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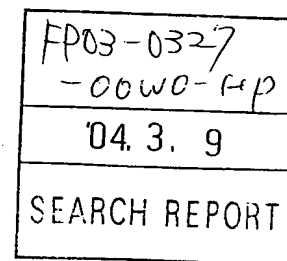
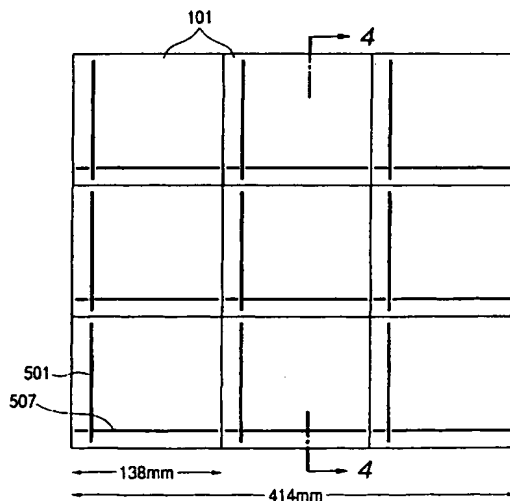
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(54) **Image pickup apparatus**

(57) This invention provides a large image pickup apparatus for, e.g., X-rays that can provide a seamless image by using a plurality of single-crystal silicon image pickup elements. More specifically, the image pickup apparatus includes an image pickup region where a plurality of pixels which include photoelectric conversion

units are arranged to pick up an object image by dividing the object image into a plurality of regions, and a scan circuit (501, 507) arranged between the plurality of photoelectric conversion units in the region to commonly process the plurality of pixels and/or signals from the plurality of pixels.

FIG. 3



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Description**BACKGROUND OF THE INVENTION****Field of the Invention**

[0001] The present invention relates to an image pickup apparatus for picking up an object image.

Related Background Art

[0002] Digitization has been progressed in various medical fields. Also in the X-ray diagnostic field, two-dimensional image pickup apparatuses have been developed to digitize images. Large image pickup apparatuses 43 cm in size at maximum are provided for mammography and chest radiography.

[0003] A large X-ray image pickup apparatus is realized by bonding, like tiles, four sensor panels using an amorphous silicon semiconductor formed on a glass substrate which is easy to increase its size. This adopts a technique for forming and using a large substrate for amorphous silicon semiconductor devices (large substrate, technique of forming elements on the substrate, and the like) which has already been established for LCDs (Liquid Crystal Displays). An example of this technique is disclosed in U.S. Patent No. 5,315,101. Fig. 1 shows a large active array matrix disclosed in this reference. Referring to Fig. 1, this active array matrix comprises substrates 1901, pixels 1902, connection leads 1903, and common terminals 1904.

[0004] A large X-ray image pickup apparatus is manufactured by using a plurality of single-crystal image pickup elements (silicon or the like). Examples of this technique are disclosed in U.S. Patent Nos. 4,323,925 and 6,005,911. Examples of single-crystal image pickup elements are CCD image pickup elements, MOS image pickup elements, and CMOS image pickup elements. Each image pickup element has performance which satisfactorily copes with an X-ray moving picture.

[0005] Fig. 2 shows an image sensor disclosed in U.S. Patent No. 4,323,925. Referring to Fig. 2, an image 2003 of an object 2001 is formed on a surface 2004 via a lens 2002. Successive optical sub-images 2005 are reduced by tapered FOPs (Fiber Optic Plates) 2006 and incident on image input surfaces 2007. Image sensor modules 2008 have non-image pickup peripheral regions 2009 to which lead lines 2010 can be connected.

[0006] The prior art shown in Fig. 1 suffers the following problems.

[0007] Only a maximum of four (2×2) sensor panels can be used to form one image because external terminals are connected to the peripheral portion to externally connect a driving circuit.

[0008] In addition, the scale of a signal processing circuit mountable on an image pickup element is restricted to allow a pixel to have only a pixel selection switch. Signal processing circuits (driver, amplifier, and the like) are

externally connected.

[0009] Amorphous silicon, which is not good in semiconductor characteristics with respect to high-speed operation, is difficult to form a large image pickup apparatus which copes with a moving picture. An amorphous silicon image pickup element, which is lower in sensitivity than a single-crystal silicon image pickup element, is difficult to cope with an X-ray moving picture which requires high sensitivity.

[0010] The prior art shown in Fig. 2 has the following problems.

[0011] Each image pickup element is small (wafer size is 8" at maximum by the current technique), so that 2×2 or more image pickup elements are required.

[0012] In a simple large image pickup apparatus using many single-crystal image pickup elements, a dead space is inevitably formed at a bonding portion between respective image pickup elements (regions for arranging peripheral circuits such as a shift register, amplifier, and the like, external terminals for exchanging external signals and power, and a protection circuit are necessary separately from a pixel region). The dead space appears as a line defect, thereby degrading the image quality. To prevent this, a tapered FOP (Fiber Optic Plate) is used to guide light from a scintillator to an image pickup element while detouring the dead space. However, extra FOPs increase the manufacturing cost. Especially a tapered FOP is very expensive.

[0013] Light from the scintillator is difficult to enter the tapered FOP depending on the taper angle. The output light quantity decreases, and the sensitivities of image pickup elements lower, thereby decreasing the sensitivity of the overall apparatus.

SUMMARY OF THE INVENTION

[0014] It is an object of the present invention to provide an image pickup apparatus having an optimal circuit arrangement.

[0015] To achieve the above object, according to an aspect of the present invention, there is provided an image pickup apparatus comprising an image pickup region where a plurality of pixels which include photoelectric conversion units are arranged to pick up an object image by dividing the object image into a plurality of regions, and a scan circuit arranged between the plurality of photoelectric conversion units in said image pickup region to commonly process the plurality of pixels or/and signals from the plurality of pixels.

[0016] According to another aspect of the present invention, there is provided an image pickup apparatus comprising an image pickup region where a plurality of pixels which include photoelectric conversion units are arranged to pick up an object image by dividing the object image into a plurality of regions, and a common processing circuit arranged between the plurality of photoelectric conversion units in the image pickup region to selectively transfer, to a horizontal output line, signals

from a vertical output line to which signals from a plurality of pixels in a vertical direction are read.

[0017] According to still another aspect of the present invention, there is provided an image pickup apparatus for dividing an object image into a plurality of regions to form one image, wherein external terminals which are connected to a wiring line sandwiched between boundary sides of first and second regions and are arranged in the first region, are not at the same positions in a direction along the boundary sides as external terminals which are connected to another wiring line sandwiched between the boundary sides and are arranged in the second region.

[0018] The above and other objects and features of the present invention will be apparent from the following description in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

[0019]

Fig. 1 is a plan view showing prior art 1;
 Fig. 2 is a view showing prior art 2;
 Fig. 3 is a plan view showing the layout of image pickup elements and the layout of scan circuits in an image pickup apparatus according to an embodiment of the present invention;
 Fig. 4 is a sectional view showing the structure of the image pickup apparatus according to the embodiment of the present invention taken along the line 4 - 4 in Fig. 3;
 Fig. 5 is a plan view showing an image pickup element and a wafer serving as the base of the image pickup element according to the embodiment of the present invention;
 Fig. 6 is a circuit diagram showing a one-pixel circuit in the image pickup element according to the embodiment of the present invention;
 Fig. 7 is a circuit diagram showing the image pickup element according to the embodiment of the present invention;
 Fig. 8 is a plan view showing the conceptual arrangement of one pixel region (cell) according to the first embodiment of the present invention;
 Fig. 9 is a plan view showing the layout of image pickup elements according to the first embodiment of the present invention;
 Fig. 10 is a circuit diagram showing the first example of a static shift register;
 Fig. 11 is a circuit diagram showing the second example of the static shift register;
 Fig. 12 is a circuit diagram showing an example of an inverter used in the shift register;
 Fig. 13 is a circuit diagram showing an example of a clocked inverter used in the static shift register;
 Fig. 14 is a circuit diagram showing an example of a dynamic shift register;

Fig. 15 is a plan view showing the layout of image pickup elements according to the second embodiment of the present invention;

Fig. 16 is a plan view showing the layout of image pickup elements according to the third embodiment of the present invention;

Fig. 17 is a plan view showing the layout of image pickup elements according to the fourth embodiment of the present invention;

Fig. 18 is a plan view showing the layout of image pickup elements according to the fifth embodiment of the present invention;

Fig. 19 is a plan view showing the layout of image pickup elements according to the sixth embodiment of the present invention;

Fig. 20 is a plan view showing the layout of one pixel region of the image pickup element according to the sixth embodiment of the present invention;

Fig. 21 is a plan view showing the layout of image pickup elements according to the eighth embodiment of the present invention;

Fig. 22 is a sectional view showing a state wherein a TAB portion is connected to an external terminal and extends between image pickup elements according to the eighth embodiment of the present invention;

Fig. 23 is an equivalent circuit diagram showing an example of a protection circuit;

Fig. 24 is a sectional view showing the structure of the protection circuit;

Fig. 25 is a plan view showing the layout of image pickup elements according to the ninth embodiment of the present invention;

Fig. 26 is a plan view showing the layout of image pickup elements according to the tenth embodiment of the present invention;

Fig. 27 is a plan view showing the layout of image pickup elements according to the eleventh embodiment of the present invention;

Fig. 28 is a plan view showing the layout of image pickup elements according to the twelfth embodiment of the present invention;

Fig. 29 is a plan view showing the layout of image pickup elements according to the thirteenth embodiment of the present invention;

Fig. 30 is a plan view showing the layout of image pickup elements according to the fourteenth embodiment of the present invention;

Fig. 31 is a plan view showing the layout of image pickup elements according to the fifteenth embodiment of the present invention; and

Fig. 32 is a view showing an X-ray diagnostic system according to the sixteenth embodiment of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0020] Items common to the following embodiments of the present invention will be described in detail with reference to Figs. 3 to 8.

[0021] Fig. 3 shows the image pickup element portion of a 414-mm square large X-ray image pickup apparatus formed by bonding, like tiles, nine 138-mm square image pickup elements 101 formed on a single semiconductor substrate by a CMOS process.

[0022] Fig. 4 shows a section taken along the line 4-4 in Fig. 3. A scintillator plate 201 made of Gd_2O_2S or CsI, wherein europium or terbium is used as an activator, is arranged on FOPs 202. X-rays 203 hit the scintillator and are converted into visible light. The visible light is detected by the image pickup elements 101. It is preferable to select the scintillator such that its light emission wavelength matches the sensitivity of the image pickup elements 101. An external processing substrate 204 has a circuit for supplying power and clocks to the image pickup elements 101, or extracting signals from the image pickup elements to process them. A TAB portion (Tape Automated Bonding) 205 electrically connects each image pickup element 101 to the external processing substrate.

[0023] The nine image pickup elements 101 are bonded so as not to substantially form gaps between them. "No gaps are substantially formed" means that no omission of pixels between the image pickup elements appears on an image formed by the nine image pickup elements. Input of clocks and power to the image pickup elements 101 and output of signals from pixels are performed between the image pickup elements 101 and the external processing substrate 204 disposed on the back of the image pickup elements 101, via the TAB portions 205 connected to electrode pads attached to the end portions of the image pickup elements. Each TAB portion 205 is sufficiently thin with respect to the pixel size, and does not generate any image defect even if the TAB portion 205 extends through the gap between the image pickup elements 101.

[0024] Fig. 5 shows one image pickup element extracted from a currently most popular 8" wafer 300. The 8" wafer 300 is an n-type wafer, which is used to form a 138-mm square CMOS image pickup element 101 as on chip by a CMOS process.

[0025] Fig. 6 is a circuit diagram showing the structure of a pixel portion which constitutes each pixel of the CMOS image pickup element 101. A photodiode (photoelectric conversion unit) 401 performs photoelectric conversion. A floating diffusion layer 402 accumulates charges. A transfer MOS transistor (transfer switch) 403 transfers charges generated by the photodiode to the floating diffusion layer. A reset MOS transistor (reset switch) 404 removes charges accumulated in the floating diffusion layer. A row selection MOS transistor (row selection switch) 405 selects a row. An amplifier MOS

transistor (pixel amplifier) 406 functions as a source follower.

[0026] Fig. 7 is a schematic view showing the whole circuit for 3×3 pixels.

[0027] The gate of each transfer switch 403 is connected to ΦTX 502 (transfer pulse) from a vertical shift register 501 as one kind of vertical scan circuit. The gate of each reset switch 404 is connected to ΦRES 503 (reset pulse) from the vertical scan circuit 501. The gate of each row selection switch 405 is connected to ΦSEL 504 (row selection pulse) from the vertical scan circuit 501.

[0028] Photoelectric conversion is done by the photodiode 401. The transfer switch 403 is OFF during the accumulation period of charges of the light quantity, and the gate of the source follower 406 which constitutes a pixel amplifier does not receive any charges photoelectrically converted by the photodiode. Before the start of accumulation, the reset switch 404 is turned on to initialize the gate of the source follower 406 which constitutes the pixel amplifier, to a proper voltage. This voltage serves as a dark level. Subsequently or simultaneously, the row selection switch 405 is turned on to operate the source follower circuit made up of a load current source and the pixel amplifier 406. At this time, the transfer switch 403 is turned on to transfer charges accumulated in the photodiode to the gate of the source follower 406 which constitutes the pixel amplifier.

[0029] Outputs from a selected row are supplied to vertical output lines (signal output lines) 505. The outputs are sequentially read out to an output amplifier 508 via a horizontal output line by driving column selection switches (multiplexers) 506 by a horizontal shift register 507 which is one kind of horizontal scan circuit.

[0030] Fig. 8 shows a state wherein a unit block (unit for selecting and driving one row) 601 of the vertical shift register 501 is formed within one pixel region (one cell) 603 together with a one-pixel circuit 602. The one-pixel circuit 602 is shown in Fig. 4. The vertical shift register is represented by a simple circuit made up of a static shift register 604 and transfer gates 605 in order to generate a transfer signal ΦTX , reset signal ΦRES , and row selection signal ΦSEL . The static shift register 604 and transfer gate 605 are driven by a signal from a clock signal line (not shown). The circuit arrangement of the shift register is not limited to this, and the shift register can take any circuit arrangement by various driving methods such as pixel addition and interlaced reading. However, the functional block is formed within one cell 603 together with the pixel circuit 602, like this embodiment, the shift register and the like are arranged within the effective pixel region, and an image pickup element having an effective pixel region on its entire surface is implemented.

[0031] The first embodiment will be described.

[0032] In the first embodiment, a vertical scan circuit such as a vertical shift register or an $n:2^n$ decoder, and a horizontal scan circuit such as a horizontal shift reg-

ister or an $n:2^n$ decoder are arranged within each pixel region (cell) in an effective pixel region.

[0033] Similarly, in the first embodiment, common processing circuits are arranged within each pixel region (cell) in the effective pixel region. The common processing circuits mean circuits for commonly processing signals from a plurality of pixels at once, such as a signal output amplifier on the final stage, a serial-to-parallel conversion multiplexer, a buffer, and various gate circuits.

[0034] To the contrary, individual circuits mean circuits each for processing a signal from only one pixel, such as a photodiode, transfer switch, pixel selection switch, and pixel output amplifier circuit.

[0035] Fig. 9 shows the arrangement (plan view) of an image pickup element according to the first embodiment.

[0036] In the first embodiment, a vertical shift register 501B and horizontal shift register 507B are arranged in the effective pixel region of the image pickup element.

[0037] One block 601 of the shift register for processing one line falls within one pixel pitch. These blocks are aligned as a series of vertical shift register blocks 501B and a series of horizontal shift register blocks 507B. These blocks extend linearly in the vertical and horizontal directions.

[0038] The respective areas of the light-receiving portions of given pixels where these shift register blocks 601 exist are slightly smaller than those of other pixels.

[0039] The shift register is implemented by a static shift register. The shift register can employ various circuit arrangements depending on the design. The first embodiment exemplifies a general circuit example. What is important is the use of static type.

[0040] According to the first embodiment, no dead space is generated around the image pickup element, and the entire surface of the image pickup element can be used as an effective pixel region.

[0041] These image pickup elements are arrayed like tiles substantially without any gaps, thereby forming a large image pickup apparatus. A large image which is almost seamless can be obtained.

[0042] The pixel size suffices to be as large as about $100\text{ }\mu\text{m}$ square to $200\text{ }\mu\text{m}$ square in a medical X-ray image pickup apparatus. Even if static shift registers using many building components are arranged, a sufficiently high aperture ratio can be realized.

[0043] Since the shift registers are arranged in the effective pixel region in the first embodiment, X-rays having passed through the scintillator plate directly hit the shift registers. This poses a problem because X-rays damage an element or generate an error.

[0044] An example of the error is a phenomenon that charges are accumulated at the interface between an insulating oxide film SiO_2 and silicon so that change of a threshold and increase of a leakage current are caused. An example of the damage is a defect generated at a p-n junction, which increases the leakage cur-

rent.

[0045] Another example of the error is similar to an error (software error) caused by the action of hot electrons that is known as a malfunction of a MOS dynamic RAM.

[0046] Generation of hot electrons by an electric field readily occurs in a short-channel structure where the electric field is high. Generation of hot electrons by X-rays are independent of the size, so the image pickup apparatus becomes unstable if being hit by X-rays regardless of the planar size.

[0047] A shift register used to drive the pixel of an image pickup element will be explained. The shift register circuit is used to sequentially transfer pulse signals.

[0048] Figs. 10 and 11 show arrangements of a static shift register circuit. This shift register circuit is disclosed in Japanese Laid-Open Patent Application No. 9-223948. One stage of the shift register circuit is constituted by one inverter and two clocked inverters in the arrangement of Fig. 10, and by three inverters and two CMOS transfer gates in the arrangement of Fig. 11. The two clocked inverters or two CMOS transfer gates receive clock signals CLK and /CLK ("/" represents negative logic) in opposite phases. Adjacent shift register circuits receive clock signals of opposite phases.

[0049] Fig. 12 is a circuit diagram showing the internal arrangement of the inverter.

[0050] Fig. 13 is a circuit diagram showing the internal arrangement of the clocked inverter. A p-channel input transistor Tr1, p-channel clocked transistor Tr2, n-channel clocked transistor Tr3, and n-channel input transistor Tr4 are connected in series between an electric power supply and ground. An output is extracted from the node between the transistors Tr2 and Tr3.

[0051] The shift register circuit used for a driving circuit is generally driven in synchronism with two clock signals of opposite phases.

[0052] Fig. 14 shows an arrangement of a dynamic shift register circuit. As shown in Fig. 14, a feedback clocked inverter (or transfer gate and inverter) is used in the static shift register, whereas a capacitor C and a transistor TR which receives a clock at its gate are interposed between inverters in the dynamic shift register, thus decreasing the number of elements and power consumption. This shift circuit is disclosed in Japanese Laid-Open Patent Application No. 5-218814. In principle, the dynamic shift register holds data by accumulating charges in the capacitor.

[0053] If any leakage occurs at a p-n junction or at the interface between an insulating layer and silicon, the capacitor cannot hold charges, and thus the dynamic shift register fails in normal operation. A dynamic shift register used under irradiation of X-rays is readily damaged by X-rays, the leakage current increases, and the dynamic shift register fails to operate, thereby degrading the reliability. Moreover, a malfunction caused by hot electrons generated by X-rays inhibits obtaining a normal image.

[0054] To the contrary, the static shift register is relatively hardly influenced by X-rays in principle, and can be used at a place where X-rays directly hit the static shift register, like the first embodiment. Hence, the use of the static shift register can implement an image pickup apparatus having high reliability with little X-ray damage or few errors.

[0055] The scan circuit can be an $n:2^n$ decoder instead of the shift register. Similar to the shift register, the $n:2^n$ decoder can sequentially scan pixels by connecting the output of a sequential increment counter to the input of the decoder. An image in an arbitrary region can be obtained by random scan by inputting to the input of the decoder the address of the region from which an image is to be obtained.

[0056] The first embodiment is suitable for a large image pickup apparatus because the use of a CMOS sensor as an image pickup element reduces power consumption.

[0057] Note that the multiplexer is built into the image pickup element in order to operate the image pickup element at high speed.

[0058] A signal is externally extracted from the image pickup element via an electrode pad. A large stray capacitance exists around the electrode pad. Thus, an amplifier 508 is arranged on the input stage of the electrode pad to compensate for the transmission characteristics of a signal.

[0059] An image pickup apparatus according to the second embodiment is the same as the first embodiment in basic arrangement, but is different from the first embodiment in the layout of shift registers.

[0060] In the second embodiment, as shown in Fig. 15, the blocks of a vertical shift register 501C lower in driving frequency than a horizontal shift register 507C are arranged in an L shape before the vertical shift register 501C crosses the horizontal shift register 507C. If the operation frequency is ignored, the blocks of the horizontal shift register 507C may be arranged in an L shape before the horizontal shift register 507C crosses the vertical shift register 501C.

[0061] If the vertical shift register 501B and horizontal shift register 507B are arranged in the effective pixel region, like the first embodiment, they inevitably cross each other. The cell at the intersection is occupied by the circuits of the vertical shift register 501B and horizontal shift register 507B, thus generating a pixel defect. Further, process defects may concentratedly occur at this portion where transistors concentrate. To avoid this, one shift register is arranged in an L shape before it crosses the other shift register, like the second embodiment. This can eliminate unnecessary wiring intersections and can simplify the layout.

[0062] In general, noise applied to a transmission path disturbs the waveform of a clock signal, and an equivalently high-speed clock signal is input to a reception circuit. A state transition circuit changes to an abnormal state and malfunctions upon reception of a clock

signal having a pulse width shorter than the normal operation range of the circuit.

[0063] For an image pickup element having an X-Y address scan circuit (shift register), if a clock signal and data signal become abnormal, e.g., stop due to any reason, the scan circuit stops or malfunctions.

[0064] Noise is readily generated in a shift register driven at high speed. Particularly when the blocks of a shift register are arranged in an L shape, like the second embodiment, the vertical shift register having a lower driving frequency is arranged in an L shape to reduce the influence of noise caused by irregularly bending wiring lines in an L shape.

[0065] An increase in stray capacitance of the shift register decreases the response speed, thus resulting in an operation error. Especially the structure of bending the shift register, like the second embodiment, requires wiring lines (a-a' and b-b' in Fig. 15) which are unnecessary in an originally linear layout, and the stray capacitance at these wiring lines may cause an adverse effect. The second embodiment reduces the influence of a response delay caused by the stray capacitance by arranging the vertical shift register having a lower driving frequency in an L shape.

[0066] An image pickup apparatus according to the third embodiment is the same as the first embodiment in basic arrangement, but is different from the first embodiment in the layout of shift registers.

[0067] In the third embodiment, as shown in Fig. 16, adjacent one-line driving blocks of a shift register are not successive on the same straight line. The respective blocks of a vertical shift register 501D are arranged on proper Y lines so as not to arrange all the blocks on one straight line in the vertical direction. The blocks of a horizontal shift register 507D are also similarly arranged.

[0068] The light-receiving region of a pixel where a shift register block exists is narrower than that of another pixel. Such pixels aligned on one straight line may provide an unnatural image. The third embodiment can reduce the image unnaturalness by appropriately distributing pixels.

[0069] An image pickup apparatus according to the fourth embodiment is the same as the first embodiment in basic arrangement, but is different from the first embodiment in the layout of shift registers.

[0070] In the fourth embodiment, as shown in Fig. 17, a circuit for scanning three lines of a shift register is set as one block, and such blocks are arranged every three lines to constitute a vertical shift register 501E. A horizontal shift register 507E is similarly constituted. A cell where the block exists is not entirely occupied by the block, and also contains a one-pixel circuit 602. As far as this condition is satisfied, the fourth embodiment includes a vertical register constituted by providing as one block a circuit for scanning a plurality of lines of a shift register and arranging such blocks for every plurality of lines.

[0071] The light-receiving region of a pixel where a

shift register block exists is narrower than that of another pixel. Such pixels aligned on one straight line may provide an unnatural image. The fourth embodiment can reduce the image unnaturalness by providing shift register circuits as one block for every three lines and arranging such blocks.

[0072] An image pickup apparatus according to the fifth embodiment is the same as the first embodiment in basic arrangement, but is different from the first embodiment in the layout of shift registers.

[0073] In the fifth embodiment, as shown in Fig. 18, a circuit for scanning n lines (n is a natural number) of a shift register is set as one block, and such blocks are arranged at every n lines to constitute a vertical shift register 501F. A horizontal shift register 507F is similarly constituted. A cell where the block exists is entirely occupied by the block, and does not have any one-pixel circuit 602.

[0074] The light-receiving region of a pixel where a shift register block exists is narrower than that of another pixel. Such pixels aligned on one straight line may provide an unnatural image. The fifth embodiment can reduce the image unnaturalness by providing shift register circuits as one for block every n lines and arranging such blocks.

[0075] In the sixth embodiment, as shown in Fig. 19, at least the light-receiving regions of pixels are equal in area and pitch. In Fig. 19, the area of a one-pixel circuit is equal between cells, and the area of the light-receiving region (not shown in Fig. 19) in the one-pixel circuit is also equal between cells. The area of the light-receiving region is preferably equal between all cells, but the area of the light-receiving region in a cell on one line at the end portion of an image pickup element may be different from that of the light-receiving region in an internal cell in order to ensure a slice margin.

[0076] Fig. 20 shows the layout of one pixel region (cell) where shift registers are arranged. This pixel region includes a light-receiving region 1801, a shift register block 1802, and a region 1803 for a switch and pixel amplifier.

[0077] In Fig. 20,

Cell Size: 150 μm square
One Block of Shift Register: 20 μm \times 150 μm
Light-Receiving Region of Pixel: 130 μm square
Region for Switch and Pixel Amplifier: 130 μm \times 20 μm

From this, the aperture ratio is 75%.

[0078] The layout of one pixel region where no shift register is arranged is obtained by eliminating the shift register block 1802 from the layout shown in Fig. 20. At least the light-receiving region of one pixel region where no shift register is arranged is the same as the light-receiving region 1801 of one pixel region (cell) where a shift register is arranged.

[0079] By simplifying the functions of the shift register

block 1802, the occupation ratio to the cell can be decreased to a degree as shown in Fig. 20. However, an increase in the number of functions widens the shift register block 1802, which is not included in the sixth embodiment unless the aperture ratio is restricted.

[0080] According to the sixth embodiment, large pixels and a properly sized shift register are used, and the light-receiving region is set equal in size between pixels. Even if the shift register and the like are arranged in the effective pixel region, the sensitivity and the barycenter of the pixel do not vary.

[0081] In the seventh embodiment, an electric power supply line is positioned as an X-ray shielding member on a shift register and/or common processing circuit in each of the first to sixth embodiments. The electric power supply line is made of, e.g., copper exhibiting a high X-ray absorptency index.

[0082] As described above, according to the first to seventh embodiments, the entire surface of an image pickup element is used as an effective pixel region, and a scan circuit and common processing circuit are interposed between pixels in the effective pixel region. Image pickup elements can be so arrayed as not to substantially form any gap between them. Even when a given image pickup element is surrounded by other image pickup elements to constitute an image pickup apparatus for forming one image by five (for a cross-shaped region) or nine (for a rectangular region of three rows \times three columns) or more image pickup elements, no image discontinuity or omission is generated between the image pickup elements.

[0083] The image pickup apparatus having the above arrangement can use not an amorphous silicon image pickup element but a single-crystal image pickup element which is difficult to increase its size, and can pick up a large or high-resolution moving picture with a high S/N ratio.

[0084] No tapered FOP need be used, which can reduce the cost of the image pickup apparatus.

[0085] The eighth embodiment will be described.

[0086] Fig. 21 shows the arrangement (plan view) of an image pickup apparatus according to the eighth embodiment.

[0087] In the eighth embodiment, a vertical shift register 501B and horizontal shift register 507B are arranged in the effective pixel region of an image pickup element.

[0088] One block 601 of the shift register for processing one line is arranged so as to fall within one pixel pitch. These blocks are aligned as a series of vertical shift register blocks 501B and a series of horizontal shift register blocks 507B. These blocks extend linearly in the vertical and horizontal directions.

[0089] The areas of the light-receiving portions of pixels where these shift register blocks 601 exist are slightly smaller than those of other pixels.

[0090] In the eighth embodiment, an external terminal 701A and protection circuits 702A are arranged within

one pixel region at the end portion of the image pickup element. The external terminal 701A has a bump 703A. As shown in Fig. 22, the bump 703A is connected to a TAB portion 205 so as to be electrically connected to an external processing substrate 204 arranged at the back of image pickup elements arrayed like tiles.

[0091] In the eighth embodiment, a one-pixel circuit 602 is not formed in one pixel region (cell) where the external terminal 701 is arranged, but is formed in one pixel region where the protection circuit 702 is arranged.

[0092] According to the eighth embodiment, the external terminal 701 and protection circuits 702 are arranged within the pixel region, and thus no region for the external terminal 701 and protection circuits 702 need be ensured on each image pickup apparatus separately from the pixel region. No dead space is substantially formed, and an image pickup element having an effective pixel region on its entire surface can be implemented. As a result, a plurality of single-crystal image pickup elements can be bonded like tiles substantially seamlessly.

[0093] The protection circuit 702A can be formed by the same CMOS process as that of the one-pixel circuit 602 and the like, and can be formed at an arbitrary position.

[0094] The protection circuit will be explained.

[0095] A protection circuit made up of a protection resistor 81 (made of, e.g., polysilicon) and protection p-n junction diodes 82 and 83, as shown in the equivalent circuit of Fig. 23, is interposed between the external terminal and internal circuit of a CMOS image pickup element in order to protect the internal circuit from electrostatic breakdown. If a high voltage is applied to the external terminal, the voltage reaches the protection diodes 82 and 83 via the protection resistor 81 and an aluminum wiring line, and one of the protection diodes 82 and 83 is turned on and discharged to an electric power supply potential Vcc or ground potential Vss. As a result, a high voltage is not applied to the internal circuit. The protection resistor 81 limits a current generated by discharge, and attenuates a high voltage to some extent before the voltage reaches the protection diodes 82 and 83.

[0096] Fig. 24 shows a general arrangement of the protection circuit 702A. A p⁺-type diffusion resistor 902 is formed in the surface of an n-type semiconductor substrate 901. One terminal of the p⁺-type diffusion resistor 902 is connected to an input electrode 907, and the other terminal is connected to an output electrode 908. A p-n junction diode on the electric power supply side is formed between the p⁺-type diffusion resistor 902 and the n-type semiconductor substrate 901. A p-well diffusion region 903 is formed at another portion of the n-type semiconductor substrate 901, and an n⁺-type diffusion region 904 connected to the output electrode 908 is formed near the surface of the p-well diffusion region 903. The n⁺-type diffusion region 904 and p-well diffusion region 903 form a p-n junction diode at the ground

side. A p⁺-type diffusion region 905 is used to ground the p-well diffusion region 903. The protection circuit 702A further comprises a field oxide layer 906, ground electrode 909, and electric power supply electrode 910.

[0097] The external terminal 701A, protection circuit 702A, and bump 703A described in the eighth embodiment are provided for an electric power supply. An actual image pickup element further comprises external terminals, protection circuits, and bumps for input signals such as a clock signal supplied to the shift register and output signals such as a read signal from each pixel.

[0098] An image pickup apparatus according to the ninth embodiment is the same as the eighth embodiment in basic arrangement, but is different from the eighth embodiment in the layout of an external terminal and protection circuit.

[0099] In the ninth embodiment, as shown in Fig. 25, an external terminal 701B having a bump 703B, and protection circuits 702B are arranged within one pixel region. The pixel pitch is 160 μm .

[0100] The ninth embodiment can minimize the number of pixel regions where no one-pixel circuit 602 is formed.

[0101] An image pickup apparatus according to the tenth embodiment is the same as the eighth embodiment in basic arrangement, but is different from the eighth embodiment in the layout of an external terminal and protection circuit.

[0102] In the tenth embodiment, as shown in Fig. 26, an external terminal 701C having a bump 703C is disposed at part of one pixel region, whereas protection circuits 702C are disposed at part of another pixel region. The pixel pitch is 160 μm .

[0103] The tenth embodiment can eliminate any pixel defect because one-pixel circuits 602 can be formed in one pixel region where the external terminal 701C is arranged and one pixel region where the protection circuits 702C are arranged.

[0104] An image pickup apparatus according to the eleventh embodiment is the same as the eighth embodiment in basic arrangement, but is different from the eighth embodiment in the layout of an external terminal and protection circuit.

[0105] In the eleventh embodiment, as shown in Fig. 27, an external terminal 701D having a bump 703D, protection diodes 82D and 83D, and protection resistor 81D are arranged within one pixel region. The pixel pitch is 100 μm .

[0106] Even with a small pixel pitch, the eleventh embodiment can minimize the number of pixel regions where no one-pixel circuit 602 is formed.

[0107] An image pickup apparatus according to the twelfth embodiment is the same as the eighth embodiment in basic arrangement, but is different from the eighth embodiment in the layout of an external terminal and protection circuit.

[0108] In the twelfth embodiment, as shown in Fig. 28, one external terminal 701E having a bump 703E over-

laps four pixel regions. The external terminal 701E occupies only part of each pixel region. A protection circuit 702E is disposed at part of one pixel region.

[0109] Even with a small pixel pitch, the twelfth embodiment can minimize the number of pixel regions where no one-pixel circuit 602 is formed.

[0110] Moreover, even with a small pixel pitch, the twelfth embodiment can eliminate any pixel defect because the one-pixel circuits 602 can be formed in one pixel region where the external terminal 701E is arranged and in one pixel region where the protection circuit 702E is arranged.

[0111] In the twelfth embodiment, a bump which is larger than a pixel is arranged. The twelfth embodiment facilitates electrical packaging because the bump is large.

[0112] An image pickup apparatus according to the thirteenth embodiment is the same as the eighth embodiment in basic arrangement, but is different from the eighth embodiment in the layout of an external terminal and protection circuit.

[0113] In the thirteenth embodiment, as shown in Fig. 29, an external terminal 701F having a bump 703F is arranged in one pixel region, and protection circuits 702F are arranged in one pixel region adjacent to the pixel region where the external terminal 701F is arranged.

[0114] The thirteenth embodiment facilitates electrical packaging because all of a pixel region is used for a bump.

[0115] An image pickup apparatus according to the fourteenth embodiment is the same as the eighth embodiment in basic arrangement, but is different from the eighth embodiment in the layout of an external terminal and protection circuit.

[0116] In the fourteenth embodiment, as shown in Fig. 30, an external terminal 701G having a bump 703G is arranged in one pixel region, and protection circuits 702G are arranged in one pixel region apart from the pixel region where the external terminal 701G is arranged.

[0117] Alternatively, the protection circuit 702G may be replaced with a protection diode, and a wiring line 1501 may be used as a protection resistor. This can increase the resistance value of the protection resistor.

[0118] The fourteenth embodiment can improve the image quality because defective pixels exist apart from each other.

[0119] The fifteenth embodiment can be applied to the eighth to fourteenth embodiments.

[0120] In the fifteenth embodiment, when image pickup elements 101A and 101B are arranged adjacent to each other, as shown in Fig. 31, an external terminal 701H having a bump 703H and an external terminal 701J having a bump 703J are shifted from each other. Alternatively, the external terminal 701H having the bump 703H and the external terminal 701J having the bump 703J are arranged so as not to exist at the same

position in a direction along adjacent sides. In Fig. 31, the external terminals 701H and 701J are arranged at the peripheral portions of the image pickup elements 101A and 101B. However, the fifteenth embodiment is not limited to this, and the external terminals 701H and 701J may be arranged inside the peripheral portions of the image pickup elements 101A and 101B as far as the external terminals 701H and 701J do not exist at the same position in a direction along adjacent sides.

[0121] This arrangement can eliminate any image defect between image pickup elements because the gap between the image pickup elements 101A and 101B can be decreased without any interference between a TAB portion 205H connected to the bump 703H and a TAB portion 205J connected to the bump 703J.

[0122] As described above, according to the eighth to fifteenth embodiments, the entire surface of an image pickup element is used as an effective pixel region, and an external terminal and protection circuit are arranged in pixels within the effective pixel region. Image pickup elements can be so arrayed as not to substantially form any gap between them. Even when a given image pickup element is surrounded by other image pickup elements to constitute an image pickup apparatus for forming one image by five (for a cross-shaped region) or nine (for a rectangular region of three rows \times three columns) or more image pickup elements, no image discontinuity or omission is generated between the image pickup elements.

[0123] The image pickup apparatus having the above arrangement can use not an amorphous silicon image pickup element but a single-crystal image pickup element which is difficult to increase its size, and can pick up a large or high-resolution moving picture with a high S/N ratio.

[0124] No tapered FOP need be used, which can reduce the cost of the image pickup apparatus.

[0125] Fig. 32 shows an application of the image pickup apparatus described in each of the first to fifteenth embodiments to an X-ray diagnostic system.

[0126] X-rays 6060 generated by an X-ray tube 6050 pass through a chest 6062 of a patient or object 6061, and is incident onto a radiation image pickup apparatus 6040 having a scintillator 201, FOP 202, image pickup element 101, and external processing substrate 204. The incident X-rays contain information about the inside of the body of the patient 6061. The scintillator emits light correspondingly to the incident X-rays, and the image pickup element photoelectrically converts the light to obtain electrical information. The information is converted into digital data, subjected to image processing by an image processor 6070, and can be observed on a display 6080 in a control room.

[0127] This information can be transferred to a remote place via a transmission circuit such as a telephone line 6090, can be displayed on a display 6081 in a doctor's room at another place or stored in a storage circuit such as an optical disk, and can also be diagnosed by a doc-

tor at the remote place. Further, the information can be recorded on a film 6110 by a film processor 6100.

[0128] Many widely different embodiments of the present invention may be constructed without departing from the spirit and scope of the present invention. It should be understood that the present invention is not limited to the specific embodiments described in the specification, except as defined in the appended claims.

Claims

1. An image pickup apparatus comprising:

an image pickup region where a plurality of pixels which include photoelectric conversion units are arranged to pick up an object image by dividing the object image into a plurality of regions; and
a scan circuit arranged between said plurality of photoelectric conversion units in said image pickup region to commonly process the plurality of pixels or/and signals from the plurality of pixels.

2. An apparatus according to claim 1, wherein said scan circuit comprises a vertical scan circuit.

3. An apparatus according to claim 1, wherein said scan circuit comprises a horizontal scan circuit.

4. An apparatus according to claim 1, wherein said scan circuit comprises a shift register.

5. An apparatus according to claim 4, wherein the shift register is of static type.

6. An apparatus according to claim 1, wherein said scan circuit comprises a decoder.

7. An apparatus according to claim 1, wherein said scan circuit occupies an entire area per one pixel region.

8. An apparatus according to claim 7, wherein said scan circuit is arranged on pixels apart from each other.

9. An apparatus according to claim 1, wherein said scan circuit occupies a partial area per one pixel region.

10. An apparatus according to claim 1, wherein said scan circuit comprises vertical and horizontal scan circuits, and the vertical scan circuit is bent so as not to cross the horizontal scan circuit.

11. An apparatus according to claim 1, wherein said

scan circuit comprises vertical and horizontal scan circuits, and the horizontal scan circuit is bent so as not to cross the vertical scan circuit.

12. An apparatus according to claim 1, wherein said scan circuit is provided on a plurality of rows or columns basis in a column or row direction.

13. An apparatus according to claim 1, wherein the scan circuit includes circuits which are arranged on a plurality of rows or columns basis to scan the plurality of rows or columns.

14. An apparatus according to claim 1, wherein an area of a light-receiving region is equal for both one pixel region where said scan circuit is arranged and one pixel region where no scan circuit is arranged.

15. An apparatus according to claim 1, wherein an electric power supply line is arranged on said scan circuit.

16. An image pickup apparatus comprising:

an image pickup region where a plurality of pixels which include photoelectric conversion units are arranged to pick up an object image by dividing the object image into a plurality of regions; and
a common processing circuit arranged between the plurality of photoelectric conversion units in said image pickup region to selectively transfer, to a horizontal output line, signals from a vertical output line to which signals from a plurality of pixels in a vertical direction are read.

17. An apparatus according to claim 16, wherein said common processing circuit comprises a multiplexer.

18. An apparatus according to claim 16, further comprising an amplifier arranged between the plurality of photoelectric conversion units to amplify signals transferred to the horizontal output line.

19. An apparatus according to claim 16, wherein said common processing circuit occupies an entire area per one pixel region.

20. An apparatus according to claim 19, wherein said common processing circuit is arranged on pixels apart from each other.

21. An apparatus according to claim 16, wherein said common processing circuit occupies a partial area per one pixel region.

22. An apparatus according to claim 16, wherein an

- electric power supply line is arranged on said common processing circuit.
23. An apparatus according to claim 1, further comprising a scintillator plate and fiber optic plate provided in front of the image pickup region.
24. An apparatus according to claim 16, further comprising a scintillator plate and fiber optic plate provided in front of said image pickup region.
25. An apparatus according to claim 23, further comprising:
- a signal processing circuit adapted to process a signal from said image pickup region;
 - a recording circuit adapted to record a signal from said signal processing circuit;
 - a display circuit adapted to display the signal from said signal processing circuit; and
 - a radiation source adapted to generate radiation.
26. An apparatus according to claim 24, further comprising:
- a signal processing circuit adapted to process a signal from said image pickup region;
 - a recording circuit adapted to record a signal from said signal processing circuit;
 - a display circuit adapted to display the signal from said signal processing circuit; and
 - a radiation source adapted to generate radiation.
27. An image pickup apparatus comprising:
- an image pickup region where a plurality of pixels which include photoelectric conversion units are arranged to pick up an object image by dividing the object image into a plurality of regions; and
 - an external terminal or/and protection circuit arranged between the plurality of photoelectric conversion units in said image pickup region.
28. An apparatus according to claim 27, wherein said protection circuit comprises a protection resistor.
29. An apparatus according to claim 27, wherein said protection circuit comprises a protection diode.
30. An apparatus according to claim 27, wherein said external terminal has a bump.
31. An apparatus according to claim 27, wherein said external terminal occupies an entire area per one pixel region.
32. An apparatus according to claim 27, wherein said external terminal occupies a partial area per one pixel region.
33. An apparatus according to claim 27, wherein said protection circuit occupies an entire area per one pixel region.
34. An apparatus according to claim 27, wherein said protection circuit occupies a partial area per one pixel region.
35. An apparatus according to claim 27, wherein said external terminal is arranged in one pixel region.
36. An apparatus according to claim 27, wherein said external terminal is arranged in a plurality of pixel regions.
37. An apparatus according to claim 36, wherein said external terminal occupies a partial area in each pixel region.
38. An apparatus according to claim 27, wherein said external terminal and said protection circuit are arranged in the same pixel region.
39. An apparatus according to claim 27, wherein said external terminal and said protection circuit are arranged side by side.
40. An apparatus according to claim 27, wherein said external terminal and said protection circuit overlap each other.
41. An apparatus according to claim 27, wherein said external terminal and said protection circuit are arranged in different pixel regions.
42. An apparatus according to claim 27, wherein the pixel region where said external terminal is arranged and the pixel region where said protection circuit is arranged are adjacent to each other.
43. An apparatus according to claim 27, wherein the pixel region where said external terminal is arranged and the pixel region where said protection circuit is arranged are apart from each other.
44. An apparatus according to claim 27, wherein a protection resistor is interposed between said external terminal and said protection circuit.
45. An apparatus according to claim 27, wherein external terminals which are connected to a wiring line sandwiched between boundary sides of first and second regions included in the plurality of regions and are arranged in the first region, are not at the

same positions in a direction along the boundary sides as external terminals which are connected to another wiring line sandwiched between the boundary sides and are arranged in the second region.

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46. An image pickup apparatus for dividing an object image into a plurality of regions to form one image, wherein external terminals which are connected to a wiring line sandwiched between boundary sides of first and second regions and are arranged in the first region, are not at the same positions in a direction along the boundary sides as external terminals which are connected to another wiring line sandwiched between the boundary sides and are arranged in the second region. 10 15
47. An apparatus according to claim 27, further comprising a scintillator plate and a fiber optic plate.
48. An apparatus according to claim 46, further comprising a scintillator plate and a fiber optic plate. 20
49. An apparatus according to claim 47, further comprising: 25
- a signal processing circuit adapted to process a signal from said image pickup region;
 - a recording circuit adapted to record a signal from said signal processing circuit;
 - a display circuit adapted to display the signal from said signal processing circuit; and 30
 - a radiation source adapted to generate radiation.
50. An apparatus according to claim 48, further comprising: 35
- a signal processing circuit adapted to process a signal from said image pickup region;
 - a recording circuit adapted to record a signal from said signal processing circuit; 40
 - a display circuit adapted to display the signal from said signal processing circuit; and
 - a radiation source adapted to generate radiation. 45

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FIG. 1

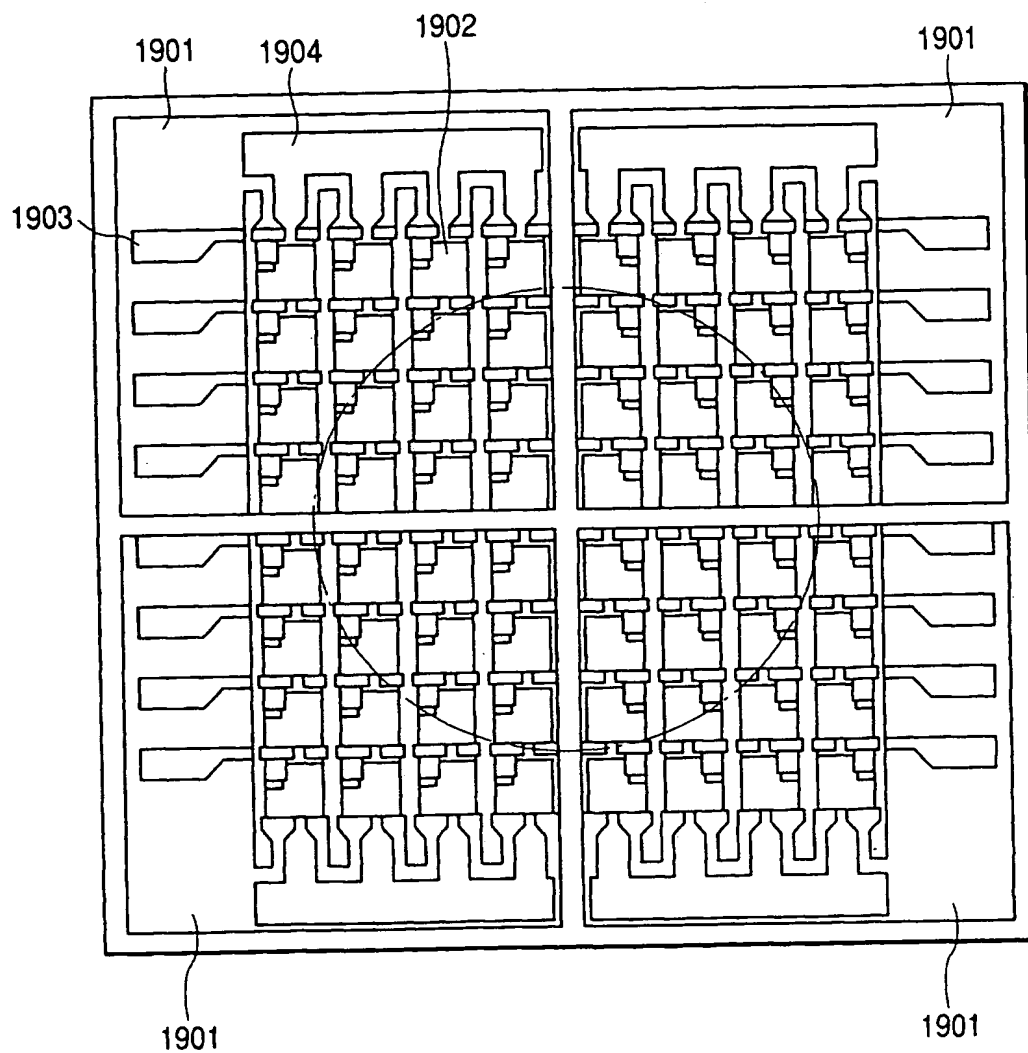


FIG. 2

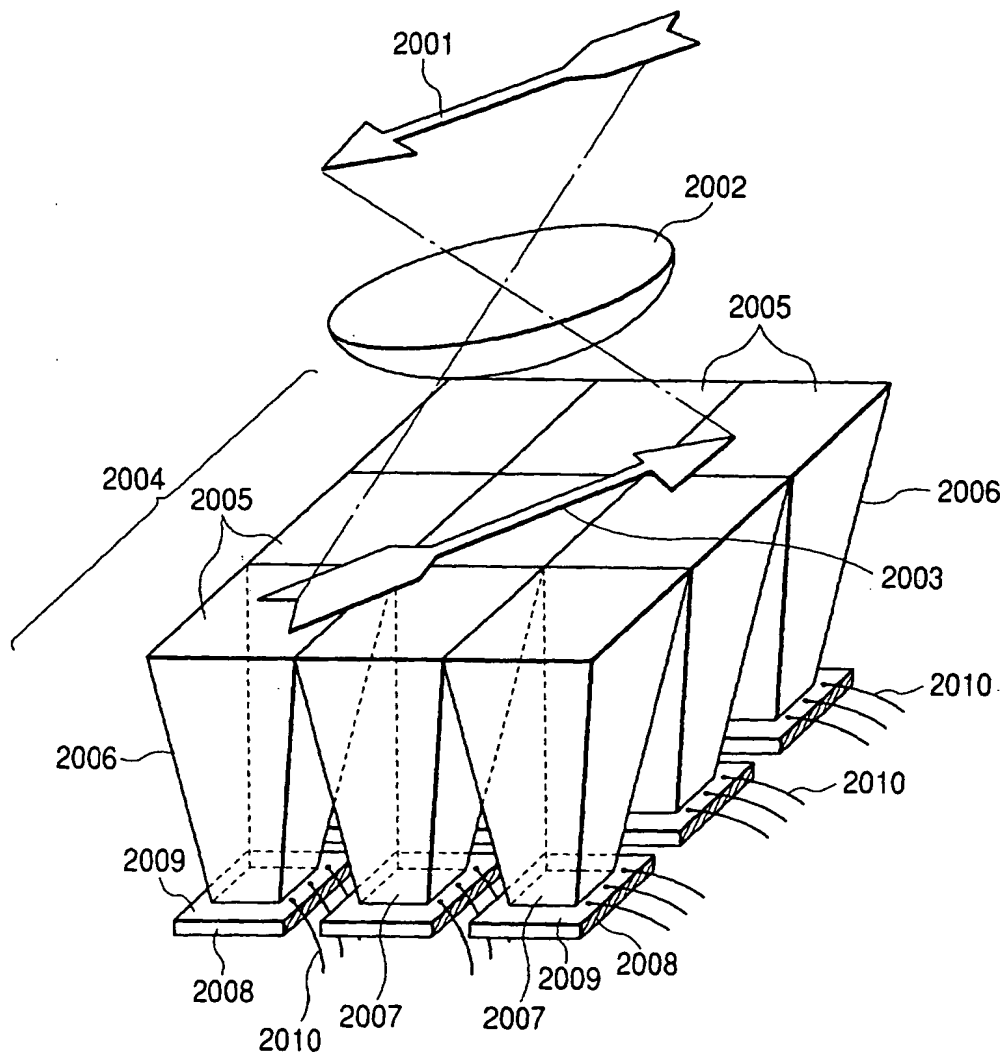


FIG. 3

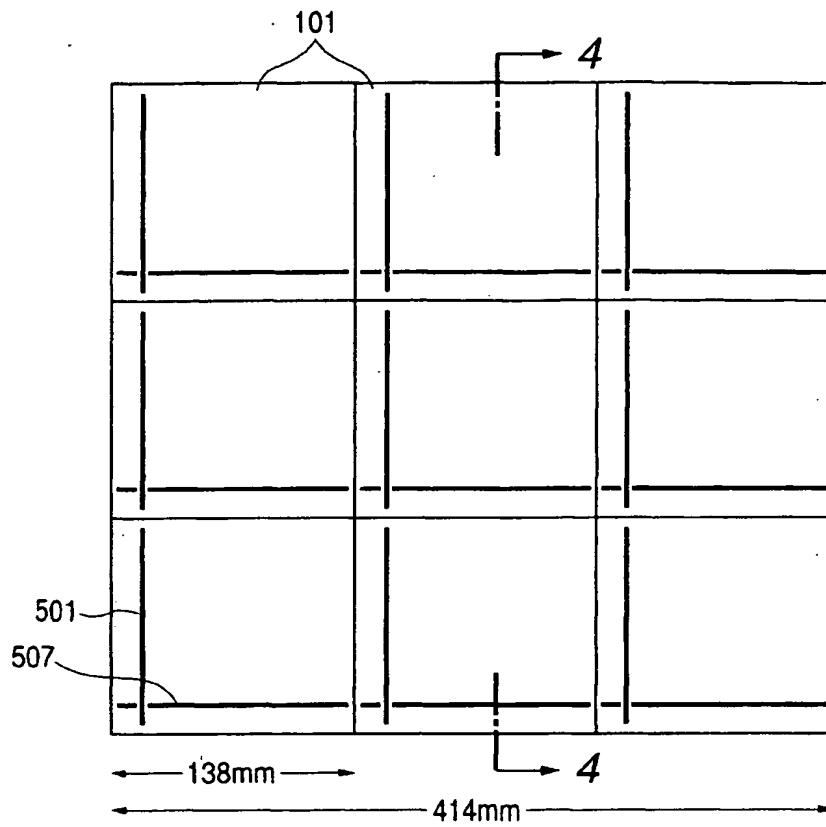


FIG. 4

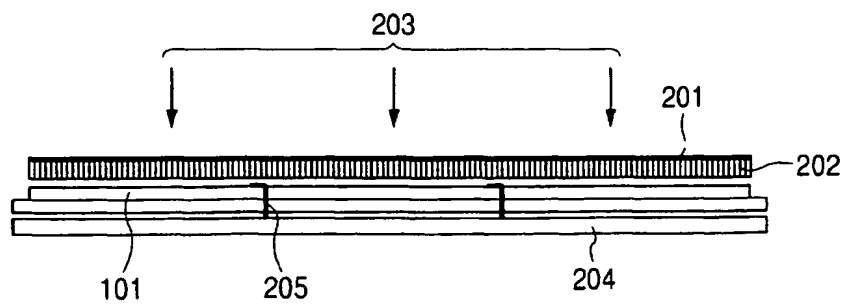


FIG. 5

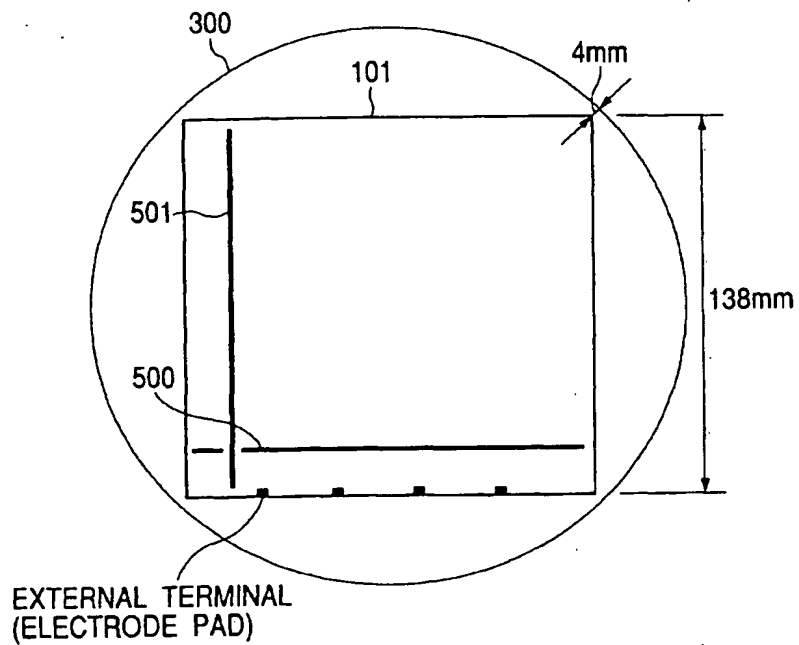


FIG. 6

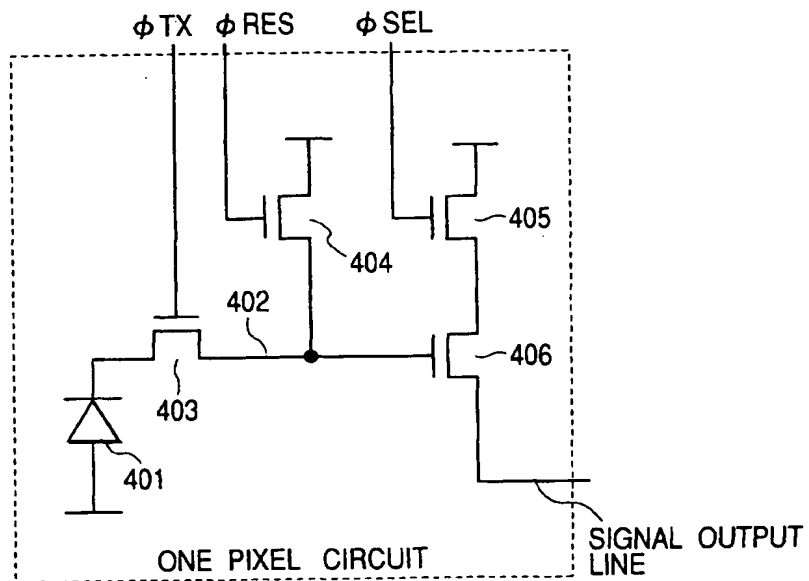


FIG. 7

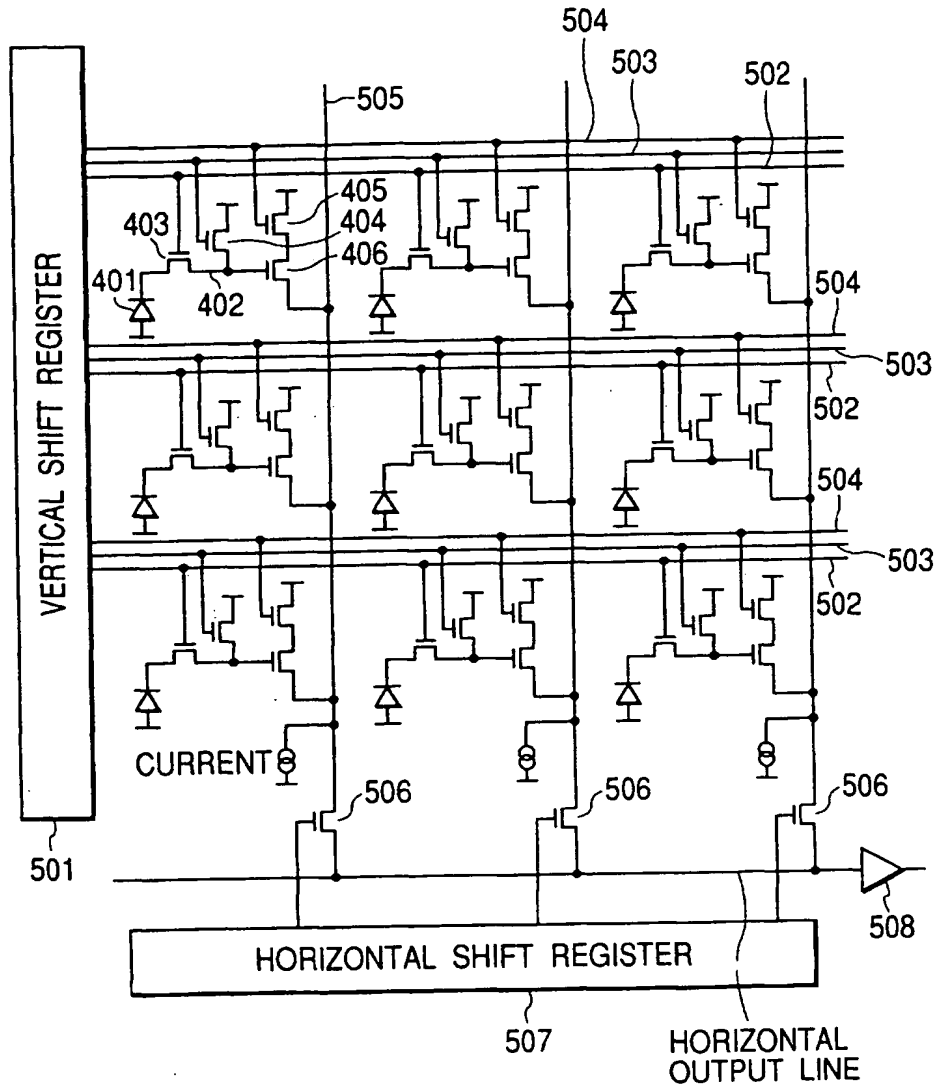


FIG. 8

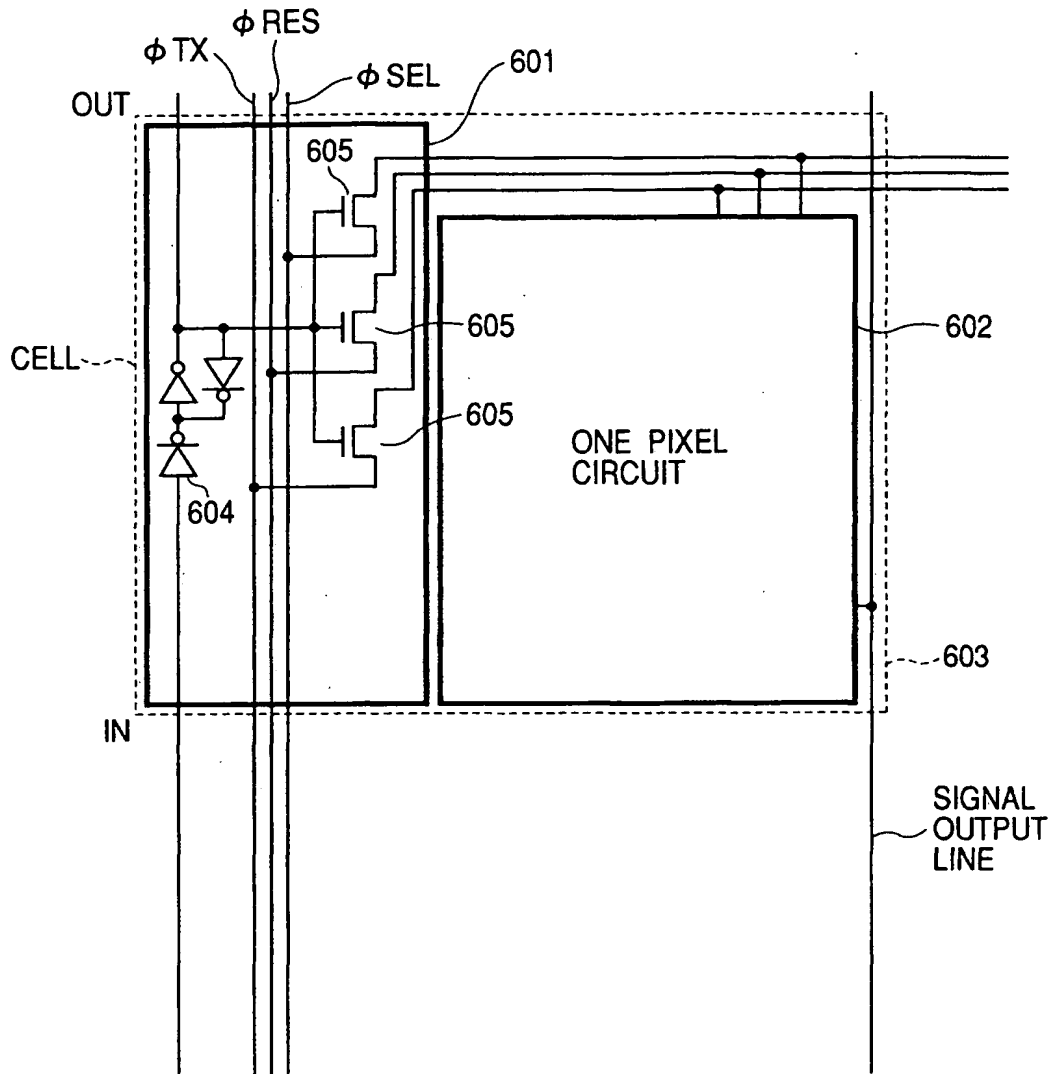


FIG. 9

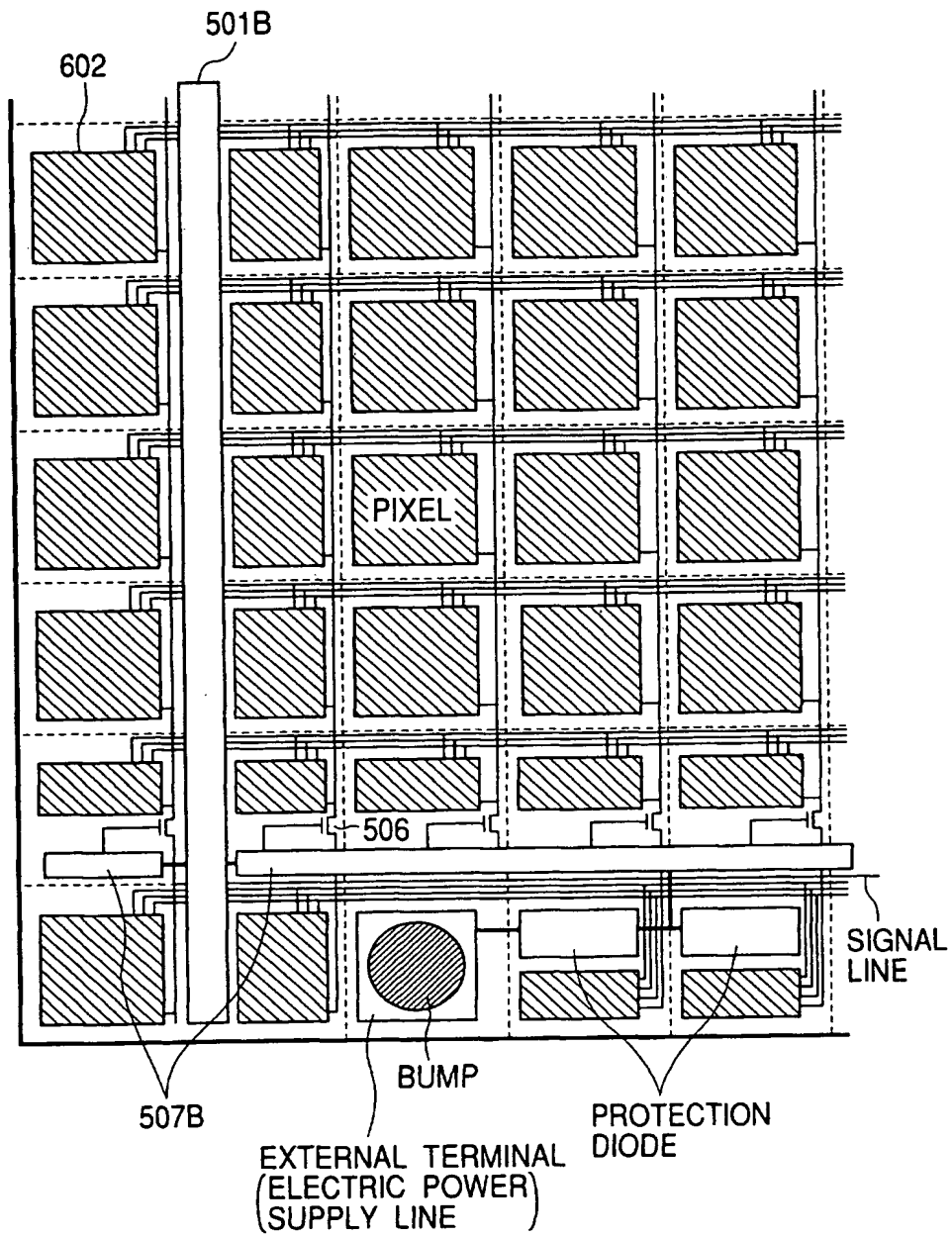


FIG. 10

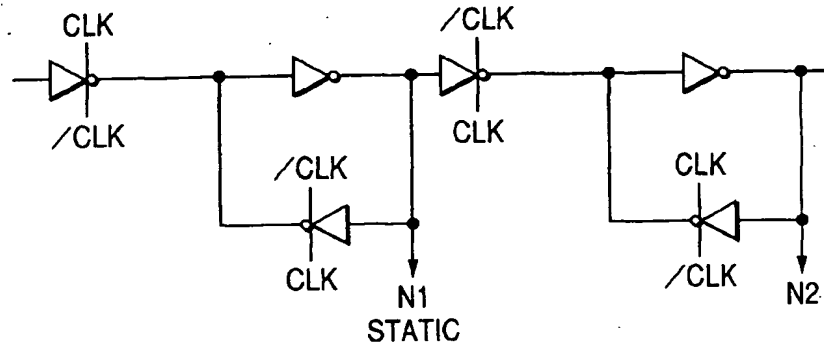


FIG. 11

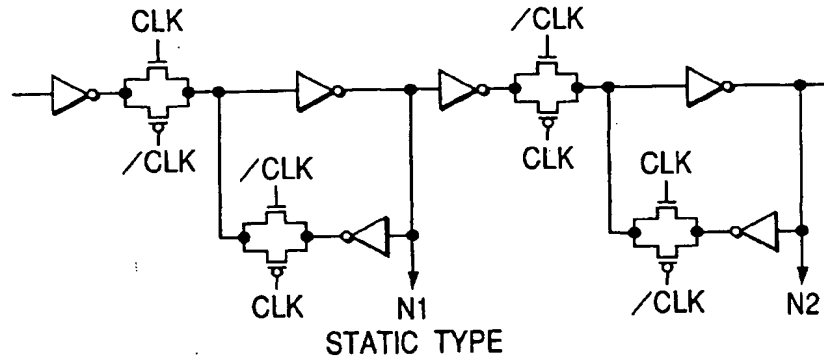


FIG. 12

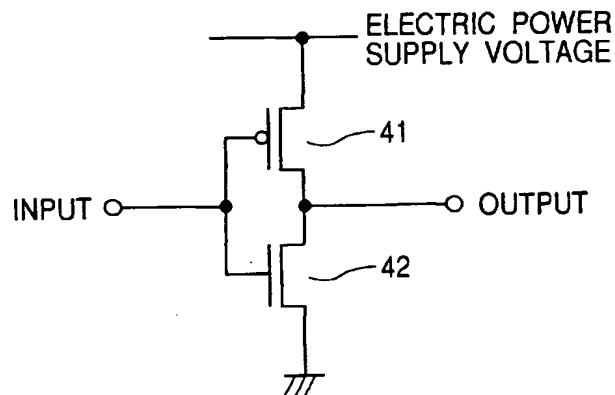


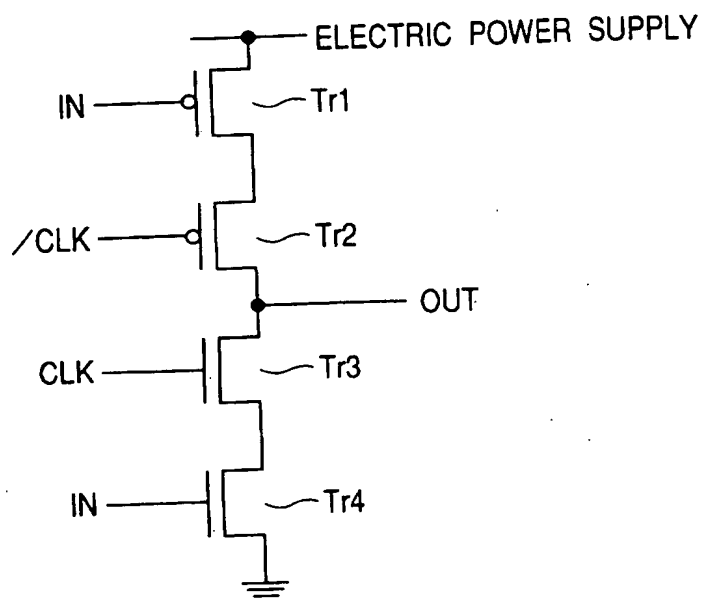
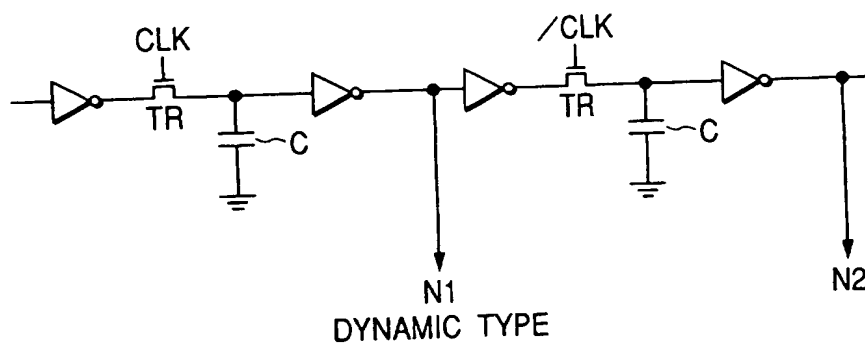
FIG. 13**FIG. 14**

FIG. 15

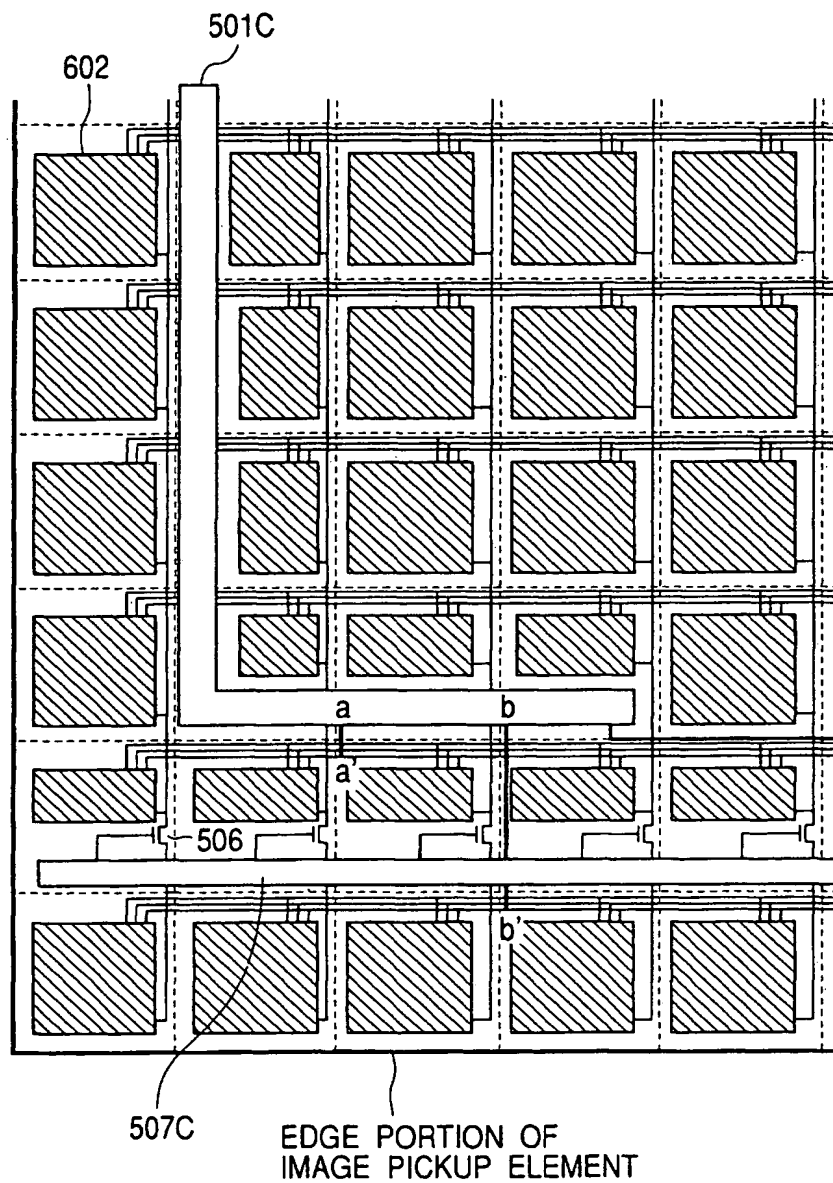


FIG. 16

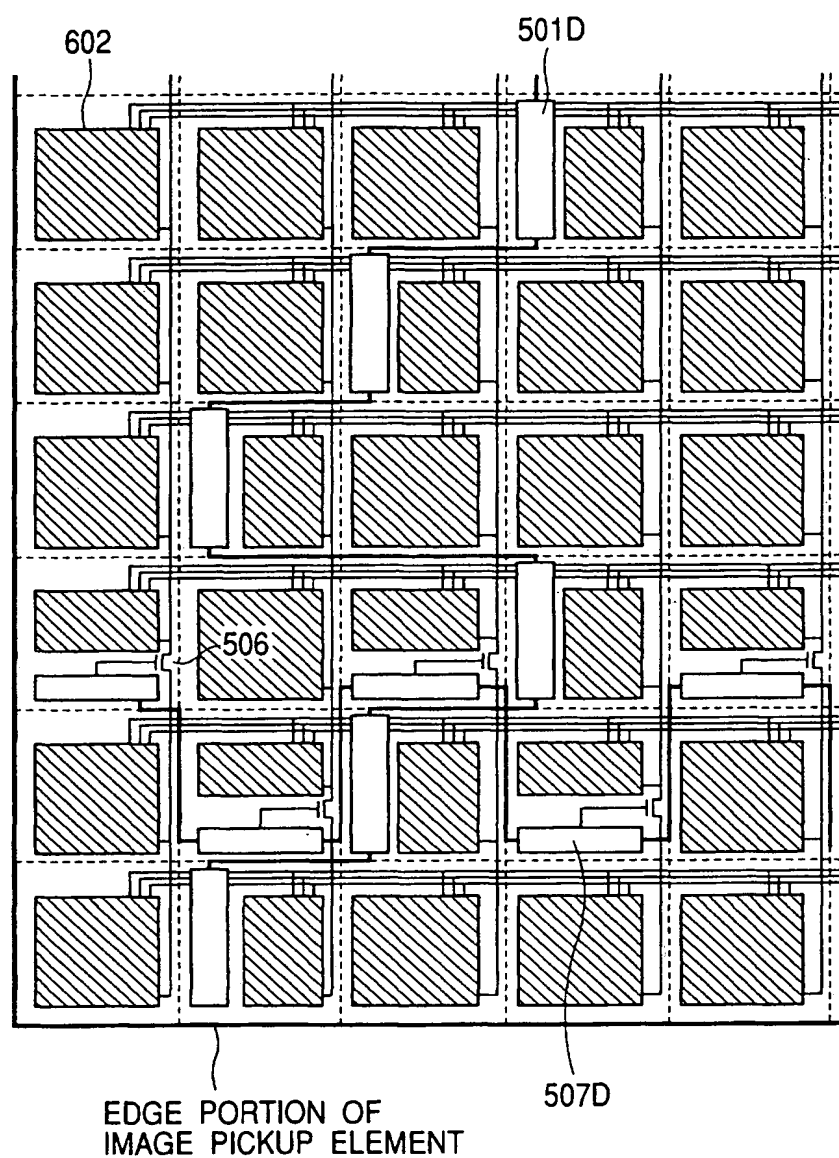


FIG. 17

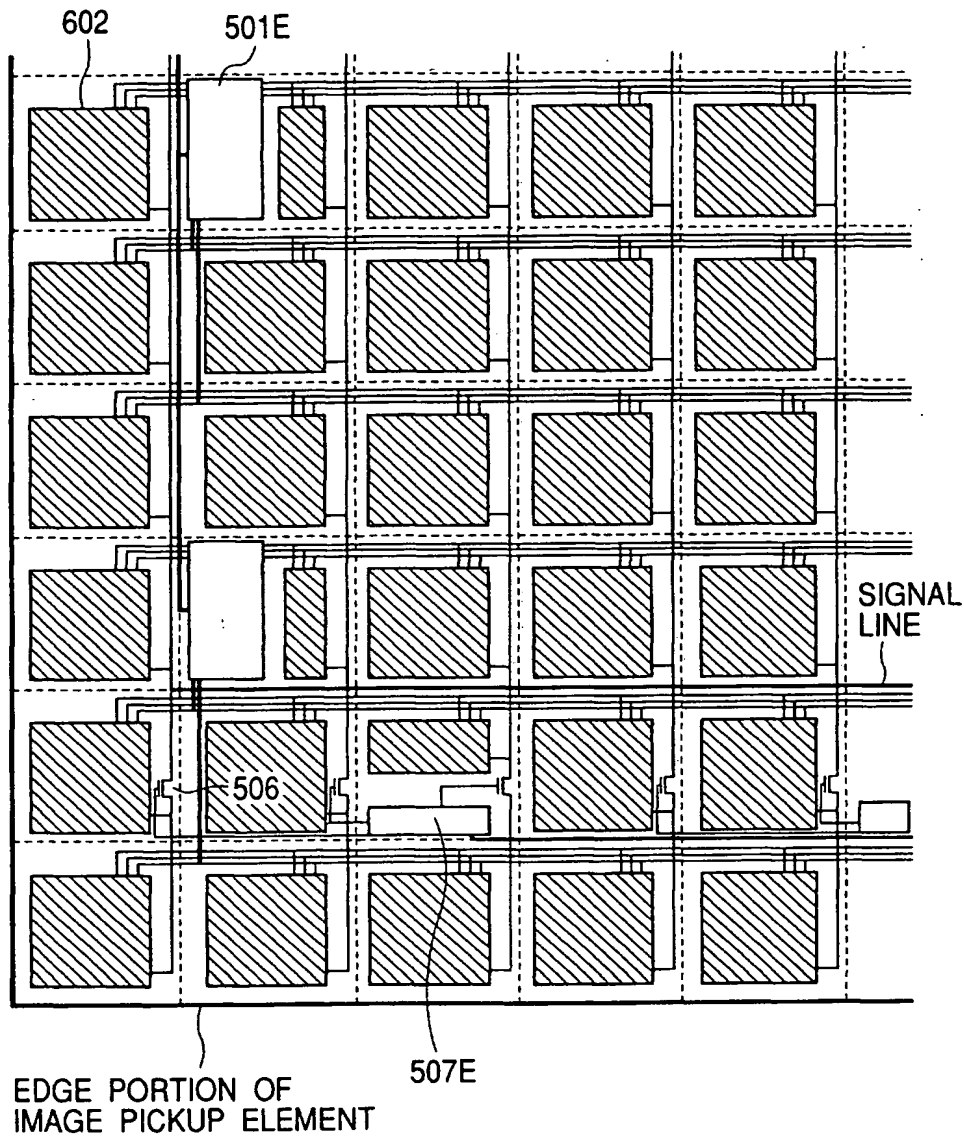


FIG. 18

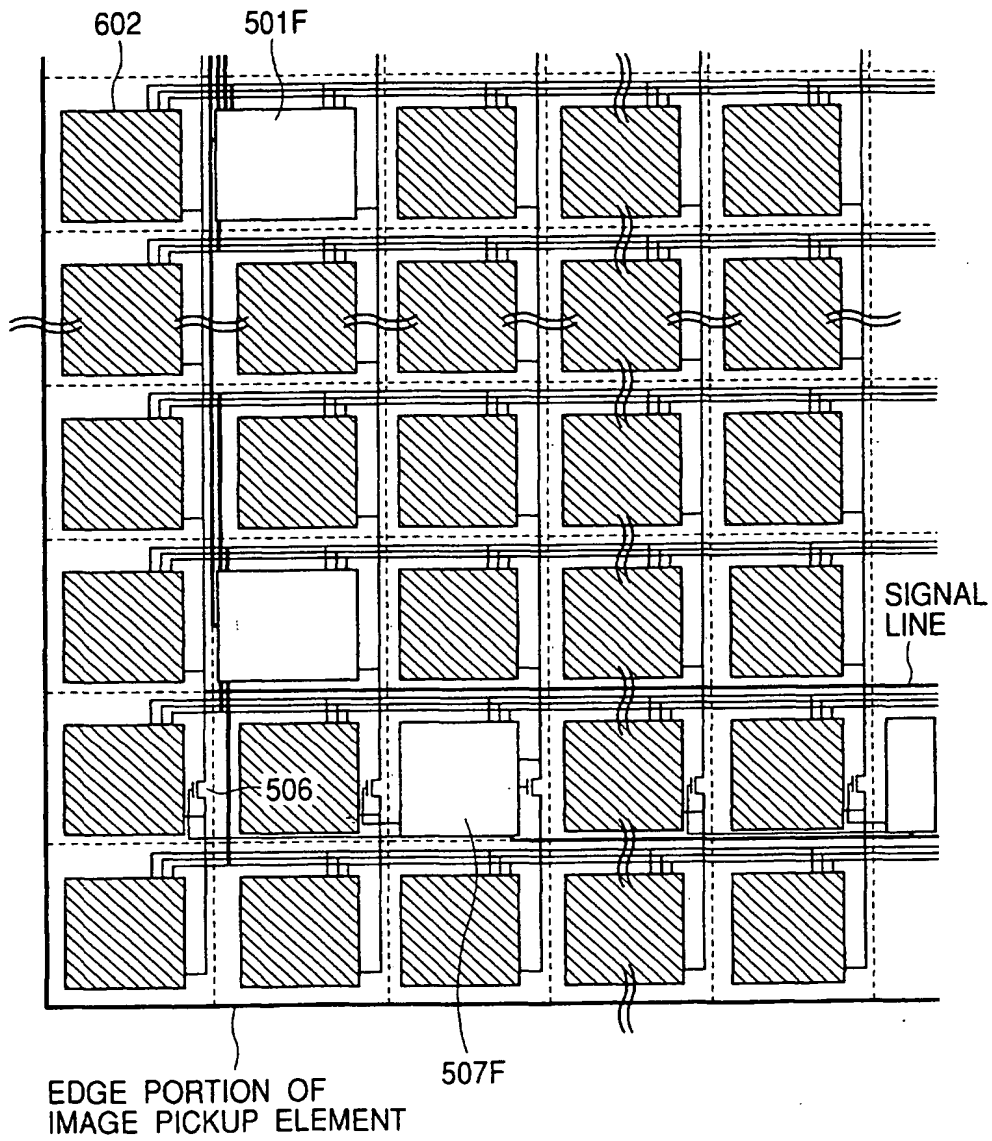


FIG. 19

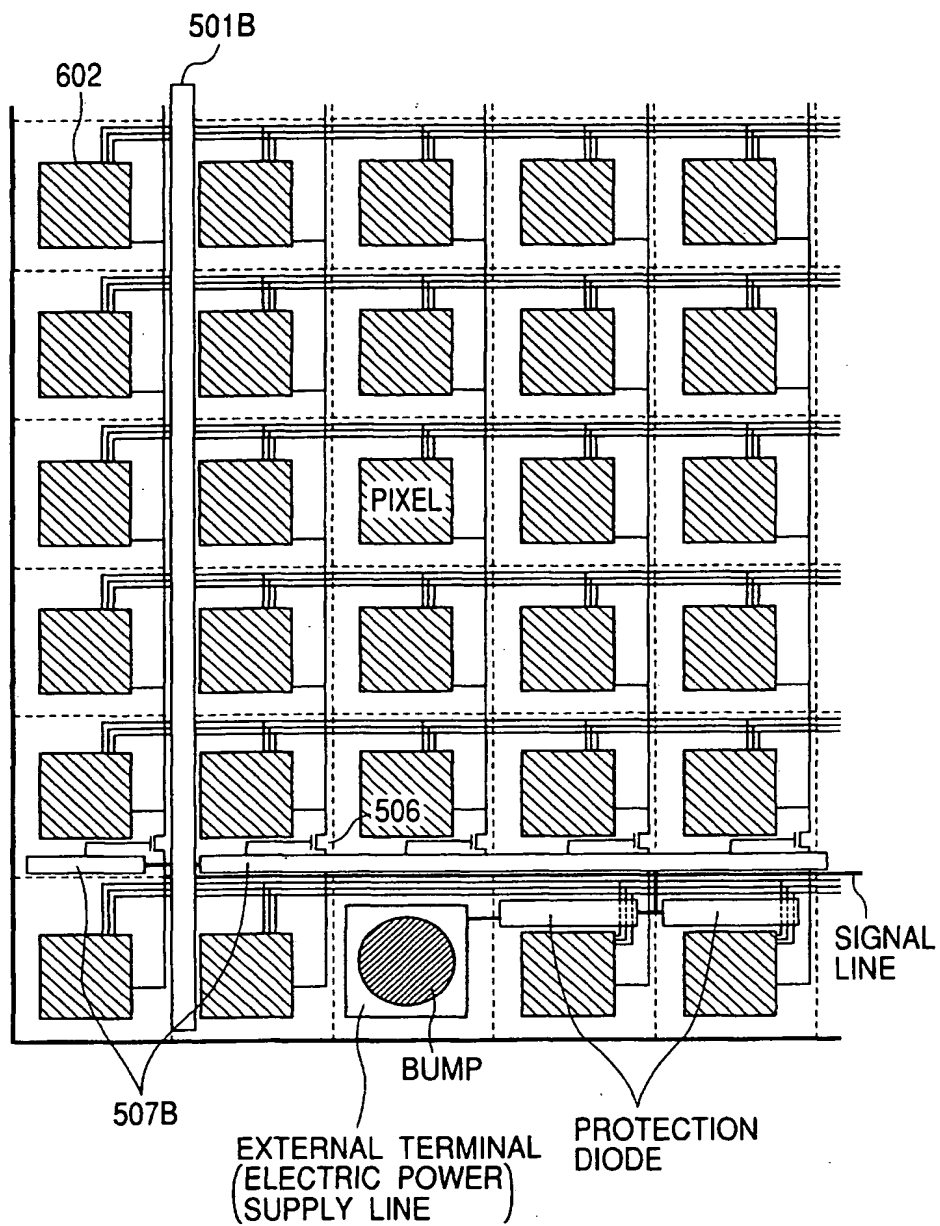


FIG. 20

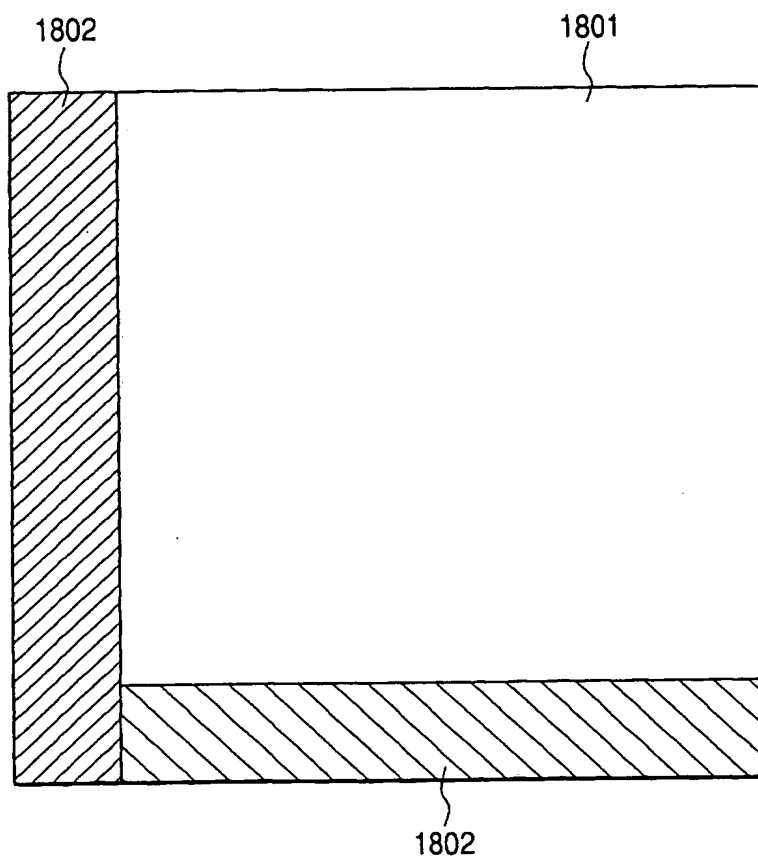


FIG. 21

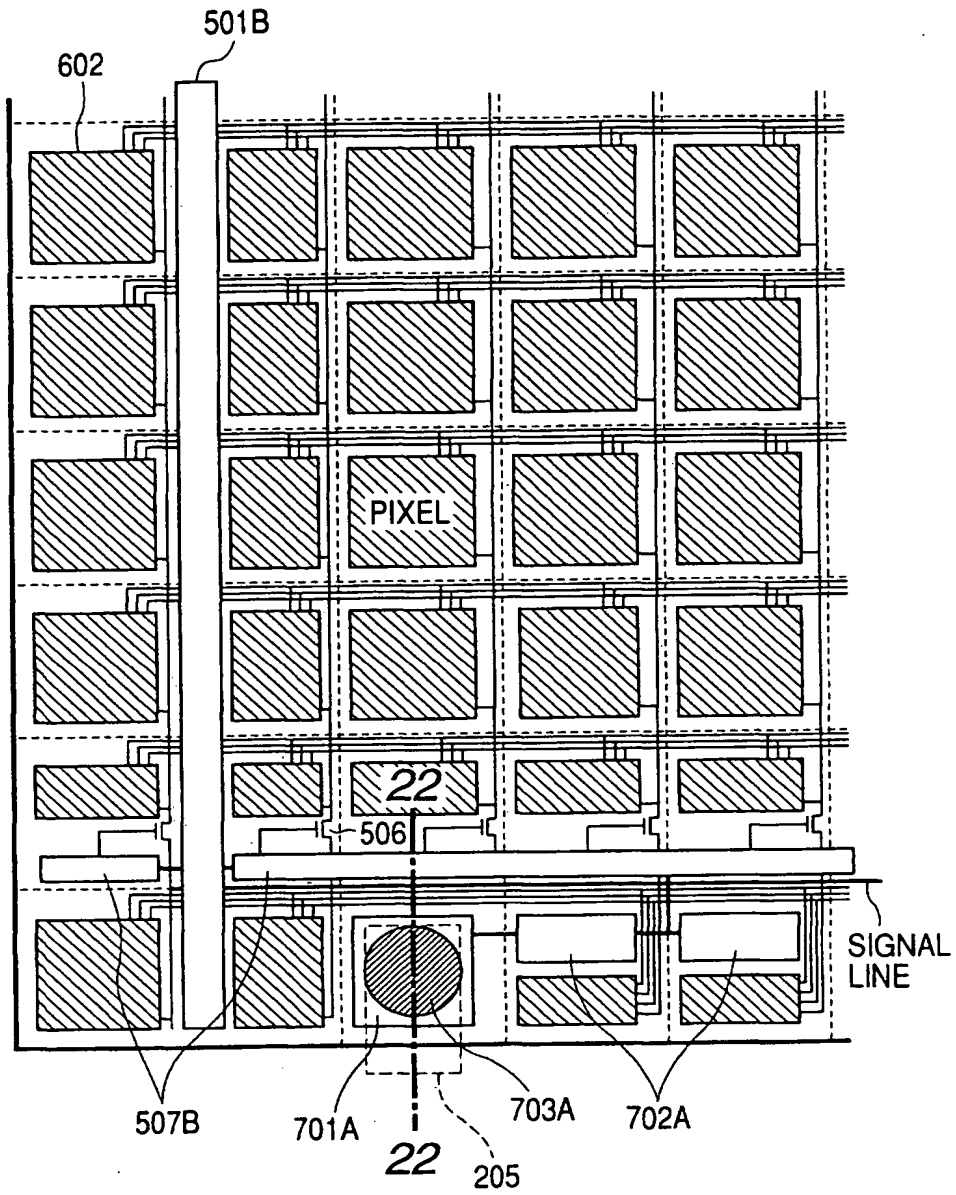


FIG. 26

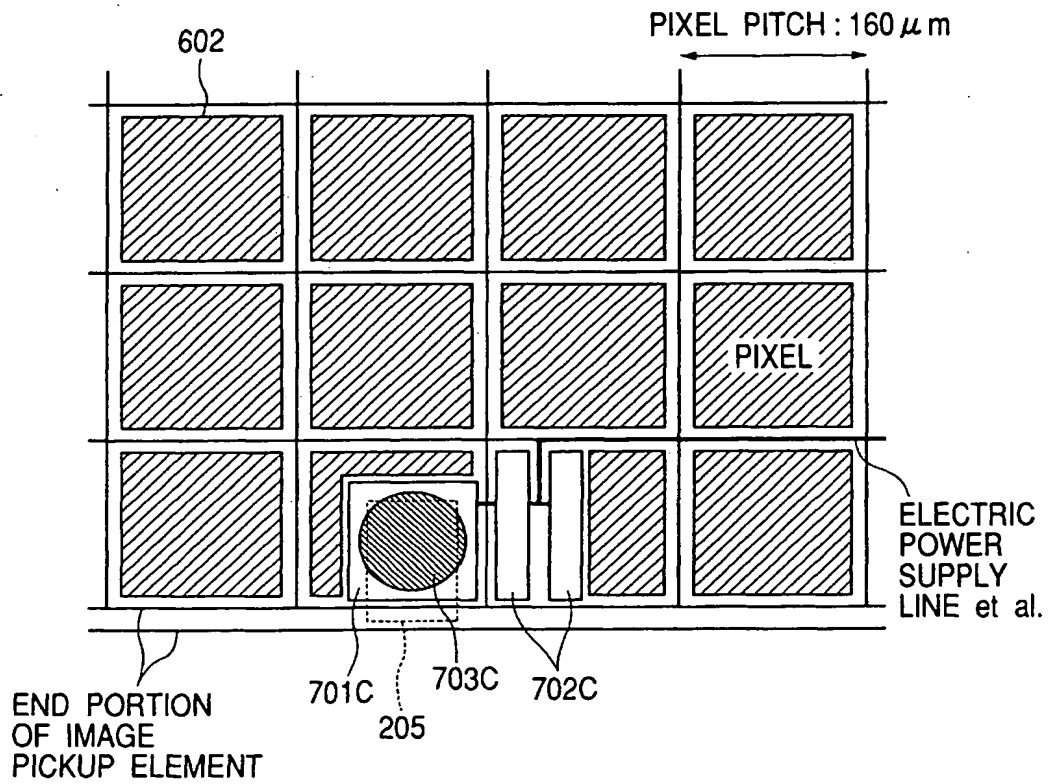


FIG. 27

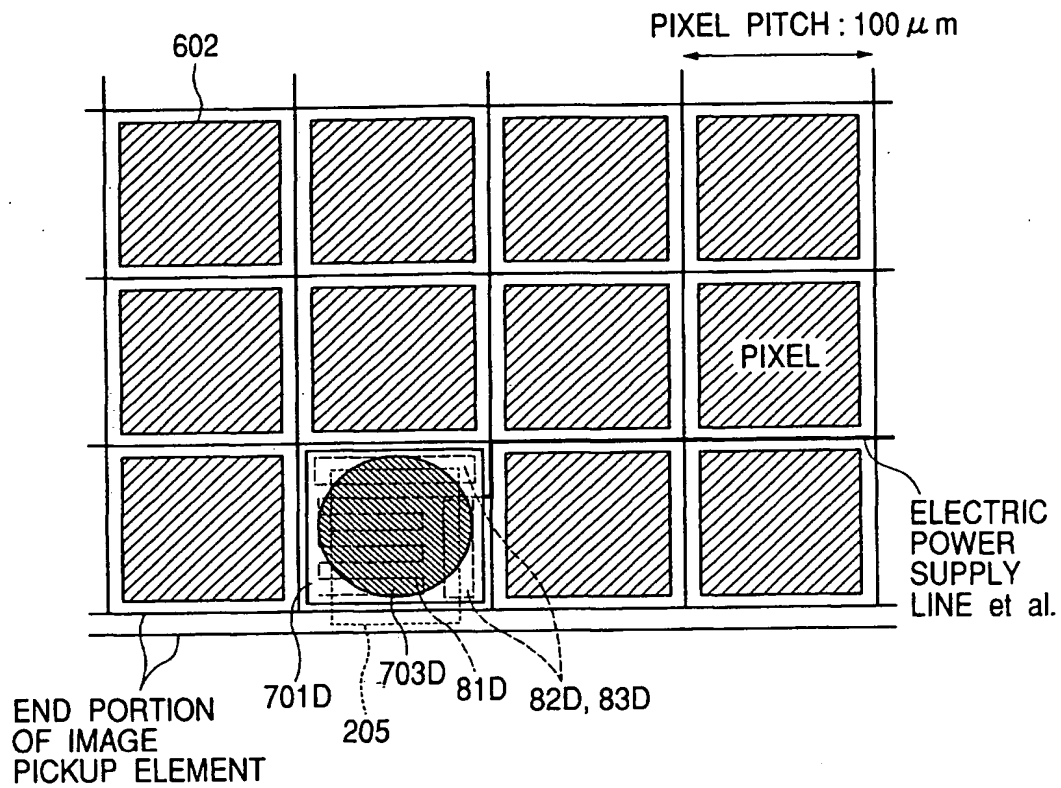


FIG. 28

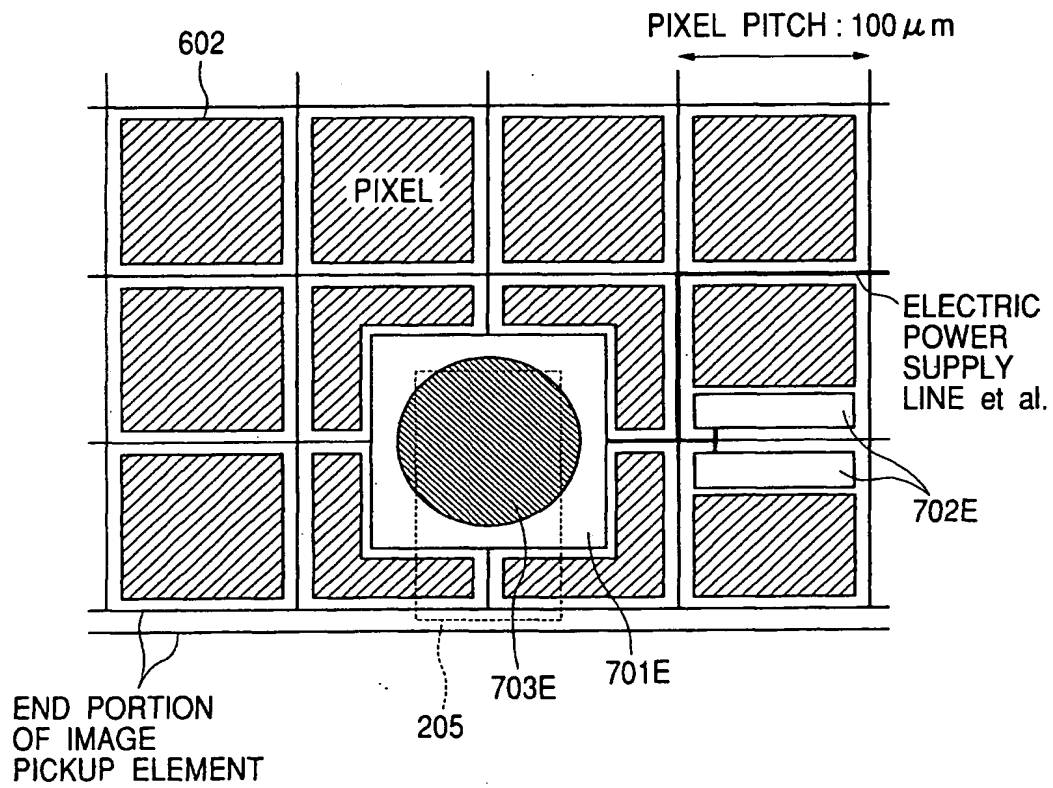


FIG. 29

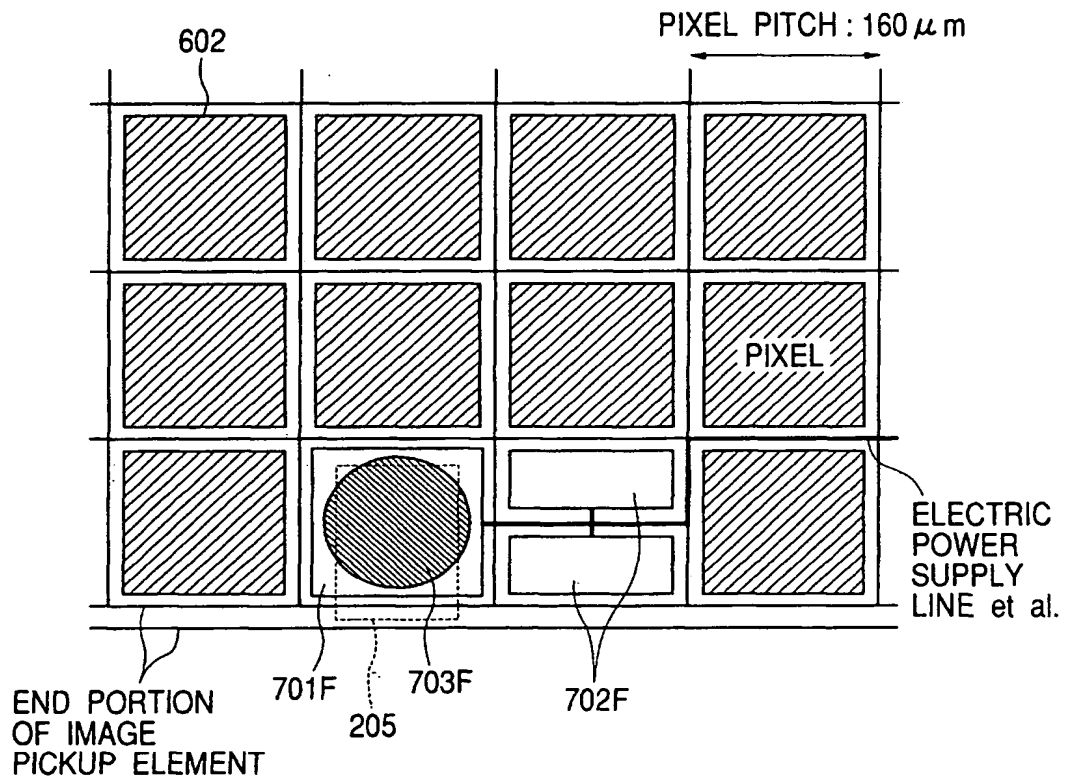


FIG. 30

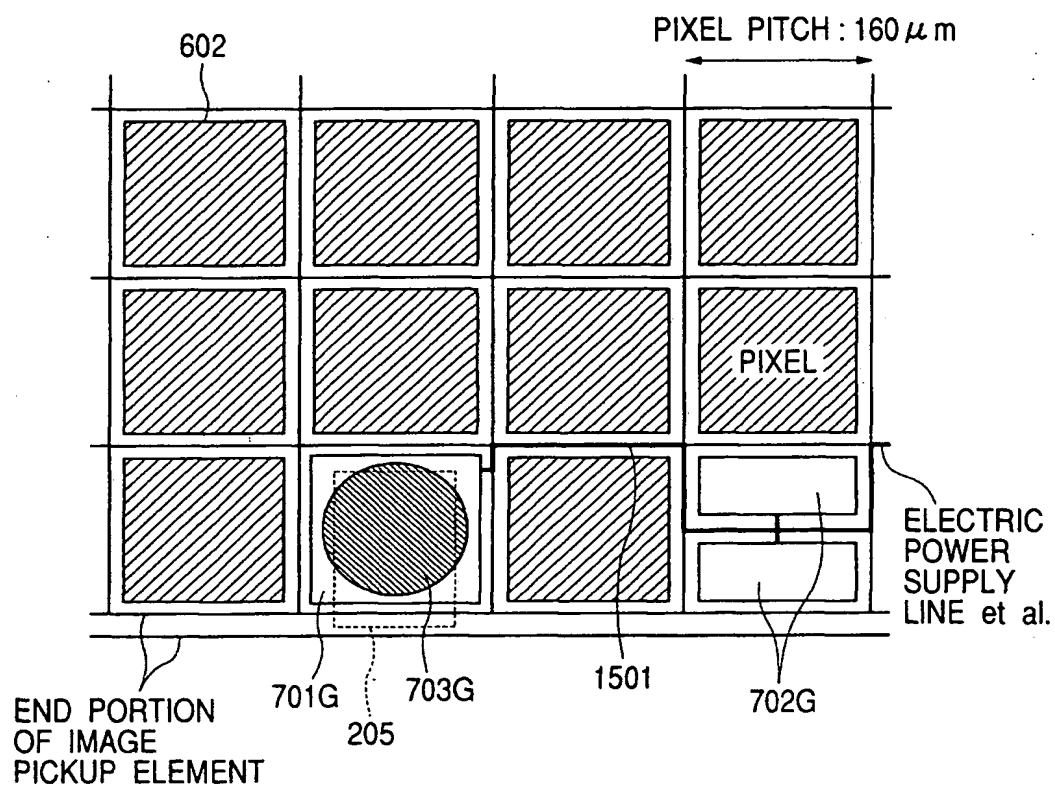


FIG. 31

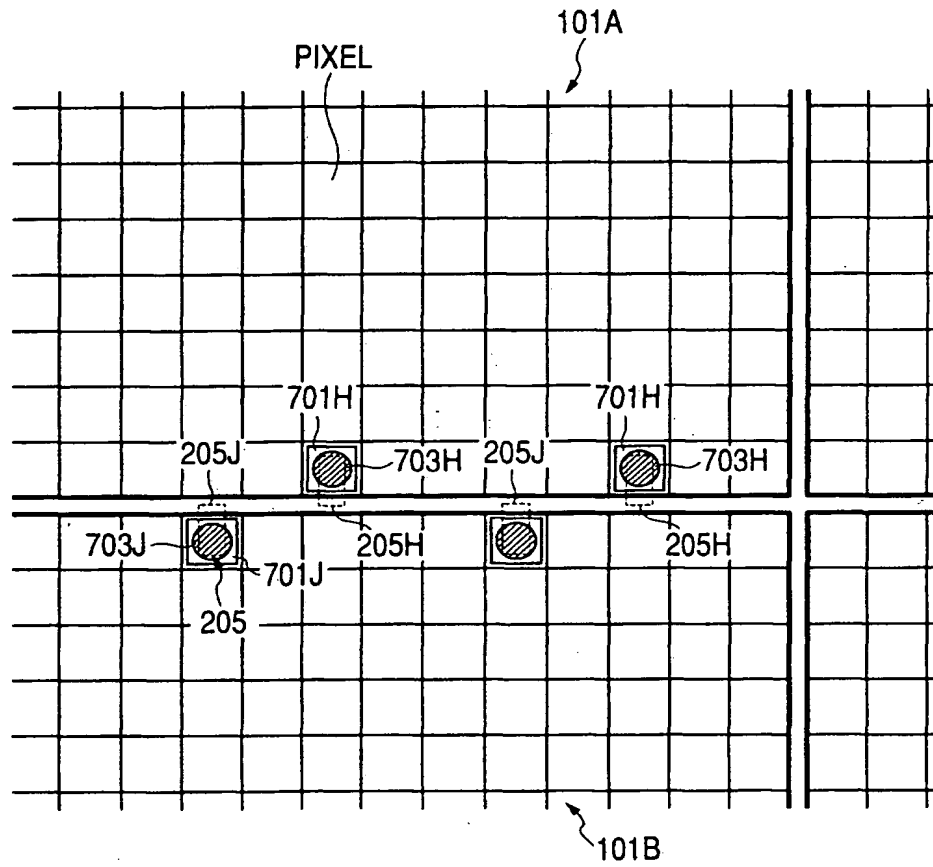


FIG. 32

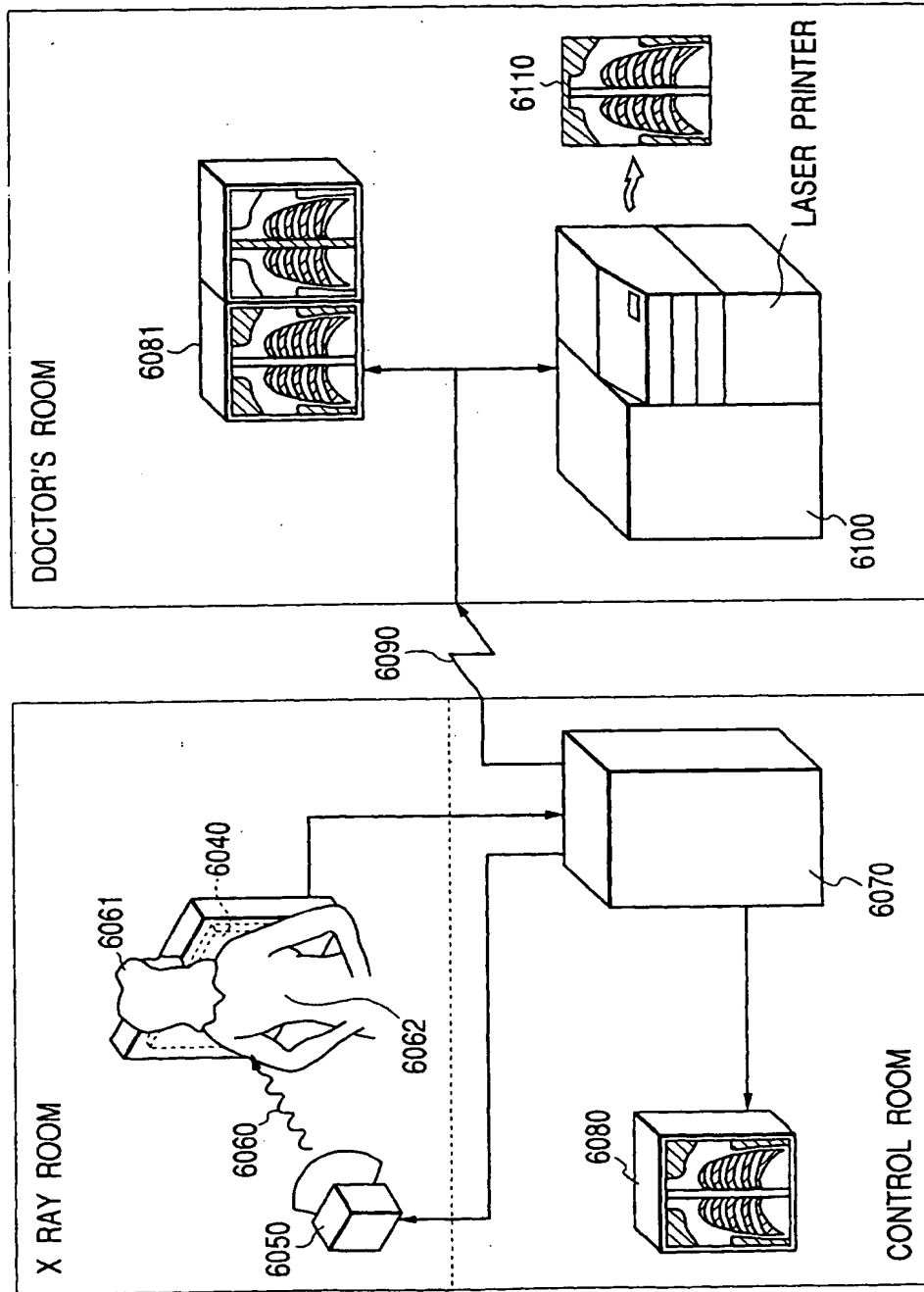


FIG. 22

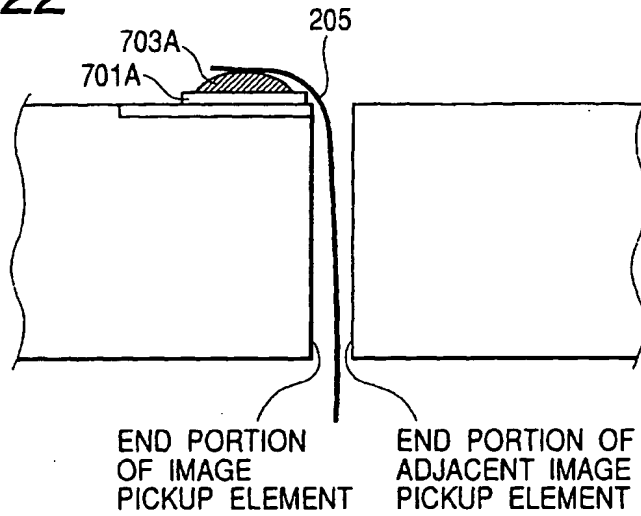


FIG. 23

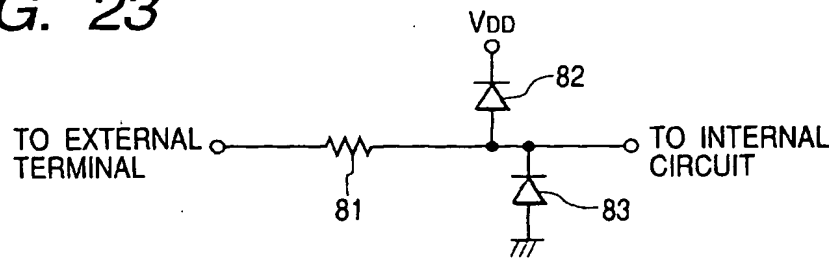


FIG. 24

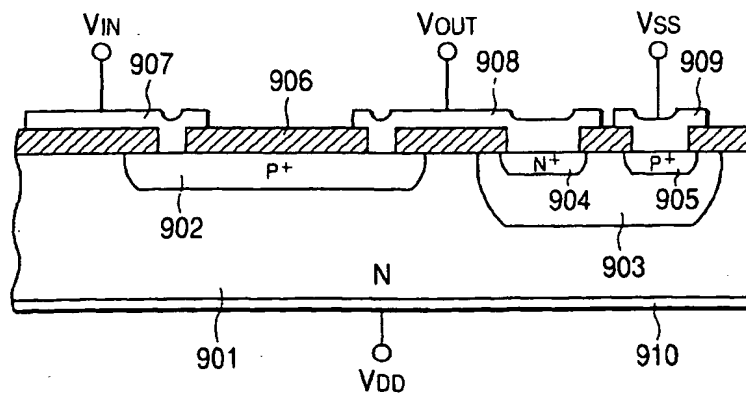


FIG. 25

